BISIMULATION ON SPEED: LOWER TIME BOUNDS*

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Abstract. More than a decade ago, Moller and Tofts published their seminal work on relating processes, which are annotated with lower time bounds, with respect to speed. Their paper has left open many questions regarding the semantic theory for the suggested bisimulation–based faster–than preorder, the MT–preorder, which have not been addressed since. The encountered difficulties concern a general compositionality result, a complete axiom system for finite processes, a convincing intuitive justification of the MT–preorder, and the abstraction from internal computation.

This article solves these difficulties by developing and employing a novel commutation lemma relating the sequencing of action and clock transitions in discrete–time process algebra. Most importantly, it is proved that the MT–preorder is fully–abstract with respect to a natural amortized preorder that uses a simple bookkeeping mechanism for deciding whether one process is faster than another. Together these results reveal the intuitive roots of the MT–preorder as a faster–than relation, while testifying to its semantic elegance. This lifts some of the barriers that have so far hampered progress in semantic theories for comparing the speed of processes.

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1. INTRODUCTION

Over the past two decades, the field of process algebra [8] has proved successful for modeling and reasoning about the communication behavior of concurrent processes. Early process algebras, such as Milner's CCS [21] and Hoare's CSP [16], have been augmented to capture other important system aspects as well, including timing behavior [7]. Many variants of *timed process algebra* that employ either discrete or continuous notions of time have been introduced, whose semantic theories are usually based on the well–studied concepts of bisimulation [22], failures [25], or testing [15].

While several approaches for comparing the efficiency of processes have been proposed in the literature [5, 24], theories for comparing timed processes with respect to speed are seeded very sparsely. The most seminal paper in the latter category was published over a decade ago [23]. In this paper, the authors Moller and Tofts argue that a *faster-than relation* on processes can only exist for those process-algebraic settings where the passage of time cannot preempt behavior, and especially not for settings involving timeout operators. For a timeout-less fragment of TCCS [22], Moller and Tofts then introduced a compositional faster-than preorder based on strong bisimulation [21], and discussed some of its underlying algebraic laws. Despite the paper's originality, the work is lacking regarding three important aspects. Firstly, the advocated preorder is not intuitively justified but appears to be an ad-hoc remedy for a compositionality problem. Secondly, the framework possesses technical weaknesses. For example, Moller and Tofts only managed to prove compositionality of their preorder for the class of regular processes, and their proposed laws for characterizing their preorder are incomplete. Thirdly, no semantic theory that abstracts from internal computation, in the sense of observation equivalence [21], is presented in [23].

The aim of this article is to put the faster-than preorder of Moller and Tofts, or MT-preorder for short, on solid semantic grounds and to highlight its intuitive roots, thereby testifying to the elegance of Moller and Tofts' approach. Technically, we add to Milner's CCS a discrete-time clock prefixing operator " σ .", interpreted as lower time bound. Intuitively, process P in σ . P is only activated after the ticking of the abstract clock σ , *i.e.*, after one time unit. The nesting of σ -prefixes then allows the specification of arbitrary delays¹, which results in a process algebra equivalent to the fragment of TCCS studied by Moller and Tofts. We refer to this algebra as Timed Asynchronous Communicating Systems with lower time bounds, or TACS^{LT}. As our first main result we prove that the MT–preorder is compositional and *fullu-abstract* with respect to a natural *amortized* preorder that uses a simple bookkeeping mechanism for deciding whether one process is faster than another. The intuition behind this amortized preorder is that the faster process must execute each action no later than the slower process does, while both processes must be functionally equivalent in the sense of strong bisimulation. To obtain this result we also establish a powerful semantic tool for reasoning within discrete-time

¹Arbitrary delays are written as prefix (n) with $n \in \mathbb{N}$ in [23].

process algebra, namely a *commutation lemma* relating the sequencing of action and clock transitions. As our second main result we provide a sound and complete *axiomatization* of the MT-preorder for the class of finite processes. This includes the provision of a simple *expansion law*, which Moller and Tofts had claimed could not exist. The twist is that this expansion law is only valid for finite processes but interestingly not for arbitrary recursive processes, which prohibits a straightforward extension of our axiomatization to non-finite processes. As our third and final main result we introduce the notion of a *weak MT-preorder* — a task that turns out to be more challenging than in other bisimulation-based settings.

Our results shed light on the nature of the MT-preorder and overcome the technical difficulties experienced by Moller and Tofts, thereby completing, generalizing, and strengthening their results and providing groundwork for advancing semantic theories that compare processes with respect to speed. This article also complements our previous work on bisimulation-based faster-than relations for timed process algebra with *upper time bounds* [19]. Indeed, several ideas and technical concepts can be carried over from the upper-time-bounds setting of [19] to the lower-time-bounds setting presented here.

The remainder of this article is organized as follows. The next section introduces our process-algebraic framework, while Sec. 3 revisits the MT-preorder's definition of [23] and establishes a general compositionality result. Secs. 4 and 5 then present our two most important theoretical contributions, namely the fullabstraction result with respect to an amortized faster-than preorder and a complete axiomatization for finite processes, respectively. The utility of our semantic faster-than theory is demonstrated by means of two simple examples in Sec. 6, before investigating an approach in Sec. 7 to abstracting from internal computation. Finally, related work is discussed in Sec. 8, and our conclusions and proposed directions for future research are given in Sec. 9. For the sake of readability we prove only those aspects of our results that are novel, and leave out proof details which are either straightforward or follow the lines of corresponding proofs in CCS.

2. TIMED ASYNCHRONOUS COMMUNICATING SYSTEMS

Our process algebra TACS^{LT} conservatively extends Milner's CCS [21] by permitting the specification of *lower time bounds* for the execution of actions and processes. These will then be used to compare processes with respect to speed. Syntactically, TACS^{LT} includes a *clock prefixing operator* " σ .", taken from Hennessy and Regan's TPL [15]. Semantically, it adopts a concept of global, discrete time in which processes are *lazy* and can always let time pass. For example, σ .P must wait for *at least* one time unit before it can start executing process P.

2.1. Syntax

The syntax of TACS^{LT} is identical to the one in [19], where we considered a faster-than preorder that relates processes on the basis of upper rather than lower time bounds. Formally, let Λ be a countably infinite set of actions not including

TABLE 1. Operational semantics for $TACS^{LT}$ (action transitions)

Act	$\frac{-}{\alpha . P \xrightarrow{\alpha} P}$	Rel	$\frac{P \xrightarrow{\alpha} P'}{P[f] \xrightarrow{f(\alpha)} P'[f]}$	Rec	$\frac{P \xrightarrow{\alpha} P'}{\mu x.P \xrightarrow{\alpha} P'[\mu x.P/x]}$
Sum1	$\frac{P \xrightarrow{\alpha} P'}{P + Q \xrightarrow{\alpha} P'}$	Sum2	$\frac{Q \xrightarrow{\alpha} Q'}{P + Q \xrightarrow{\alpha} Q'}$	Res	$\frac{P \xrightarrow{\alpha} P'}{P \setminus L \xrightarrow{\alpha} P' \setminus L} \ \alpha \notin L \cup \overline{L}$
Com1	$\frac{P \xrightarrow{\alpha} P'}{P Q \xrightarrow{\alpha} P' Q}$	Com2	$\frac{Q \xrightarrow{\alpha} Q'}{P Q \xrightarrow{\alpha} P Q'}$	Com3	$\frac{P \xrightarrow{a} P' Q \xrightarrow{\overline{a}} Q'}{P Q \xrightarrow{\tau} P' Q'}$

the distinguished unobservable, *internal* action τ . With every $a \in \Lambda$ we associate a *complementary action* \overline{a} . We define $\overline{\Lambda} =_{\mathrm{df}} \{\overline{a} \mid a \in \Lambda\}$ and take \mathcal{A} to denote the set $\Lambda \cup \overline{\Lambda} \cup \{\tau\}$. Complementation is lifted to $\Lambda \cup \overline{\Lambda}$ by defining $\overline{\overline{a}} =_{\mathrm{df}} a$. As in CCS [21], an action a communicates with its complement \overline{a} to produce the internal action τ . We let a, b, \ldots range over $\Lambda \cup \overline{\Lambda}, \alpha, \beta, \ldots$ over \mathcal{A} , and represent clock ticks by σ . The syntax of TACS^{LT} is defined as follows:

 $P ::= \mathbf{0} \mid x \mid \alpha.P \mid \sigma.P \mid P+P \mid P|P \mid P \setminus L \mid P[f] \mid \mu x.P$

where x is a variable taken from a countably infinite set \mathcal{V} of variables, $L \subseteq \mathcal{A} \setminus \{\tau\}$ is a restriction set, and $f : \mathcal{A} \to \mathcal{A}$ is a finite relabeling. A finite relabeling satisfies the properties $f(\tau) = \tau$, $f(\overline{\alpha}) = \overline{f(\alpha)}$, and $|\{\alpha \mid f(\alpha) \neq \alpha\}| < \infty$. The set of all terms is abbreviated by $\widehat{\mathcal{P}}$, and we define $\overline{L} =_{df} \{\overline{\alpha} \mid \alpha \in L\}$. Moreover, we use the standard definition for the semantic sort $\operatorname{sort}(P) \subseteq \Lambda \cup \overline{\Lambda}$ of some term P, open and closed terms, and contexts (terms with a "hole"). A variable is called guarded in a term if each occurrence of the variable is within the scope of an action or clock prefix. Moreover, we require for terms of the form $\mu x.P$ that x is guarded in P. We refer to closed and guarded terms as processes, with the set of all processes written as \mathcal{P} , and write \equiv for syntactic equality.

2.2. Semantics

The operational semantics of a TACS^{LT} term $P \in \widehat{\mathcal{P}}$ is given by a labeled transition system $\langle \widehat{\mathcal{P}}, \mathcal{A} \cup \{\sigma\}, \dots, P \rangle$, where $\widehat{\mathcal{P}}$ is the set of states, $\mathcal{A} \cup \{\sigma\}$ the alphabet, $\longrightarrow \subseteq \widehat{\mathcal{P}} \times (\mathcal{A} \cup \{\sigma\}) \times \widehat{\mathcal{P}}$ the transition relation, and P the start state. Transitions labeled with an action α are called *action transitions* which, like in CCS, are local handshake communications in which two processes may synchronize to take a joint state change together. Transitions labeled with the clock symbol σ are called *clock transitions* representing a recurrent global synchronization which encodes the progress of time.

TABLE 2. Operational semantics for TACS^{LT} (clock transitions)

tNil	$\frac{-}{0 \xrightarrow{\sigma} 0}$	tRec $\xrightarrow{P \xrightarrow{\sigma} P'}{\mu x.P \xrightarrow{\sigma} P'[\mu x.P/x]}$	tRes	$\frac{P \xrightarrow{\sigma} P'}{P \setminus L \xrightarrow{\sigma} P' \setminus L}$
tAct	$\frac{-}{\alpha . P \xrightarrow{\sigma} \alpha . P}$	tSum $\frac{P \xrightarrow{\sigma} P' Q \xrightarrow{\sigma} Q'}{P + Q \xrightarrow{\sigma} P' + Q'}$	tRel	$\frac{P \xrightarrow{\sigma} P'}{P[f] \xrightarrow{\sigma} P'[f]}$
tPre	$\frac{-}{\sigma . P \xrightarrow{\sigma} P}$	tCom $\xrightarrow{P \xrightarrow{\sigma} P'} Q \xrightarrow{\sigma} Q'$ $P Q \xrightarrow{\sigma} P' Q'$		

The operational semantics for action and clock transitions can be defined via the structural operational rules shown in Tables 1 and 2, respectively. As usual, we write $P \xrightarrow{\gamma} P'$ instead of $\langle P, \gamma, P' \rangle \in \longrightarrow$, for $\gamma \in \mathcal{A} \cup \{\sigma\}$, and say that P may engage in γ and thereafter behave like P'. Sometimes it is also convenient to write (i) $P \xrightarrow{\gamma}$ for $\exists P'. P \xrightarrow{\gamma} P'$, (ii) $\xrightarrow{\sigma}^k$ for $k \in \mathbb{N}$ consecutive clock transitions, with \mathbb{N} including 0, and (iii) $P \xrightarrow{w} P'$, where either $w = \epsilon$ and $P \equiv P'$, or $w = \gamma w'$ for some $\gamma \in \mathcal{A} \cup \{\sigma\}$ and $w' \in (\mathcal{A} \cup \{\sigma\})^*$, and $\exists P''. P \xrightarrow{\gamma} P'' \xrightarrow{w'} P'$.

The *action-prefix* term αP may engage in action α and then behave like P. It may also *idle*, *i.e.*, engage in a clock transition to itself, as process **0** does. The *clock-prefix* term σ . *P* can engage in a clock transition to *P* and ensures that there is a delay of at least one time unit before P is activated. The summation operator + denotes nondeterministic choice: P + Q may behave like P or Q; according to the deterministic nature of time, a clock transition does not resolve choices. The restriction operator L prohibits the execution of actions in $L \cup \overline{L}$ and, thus, permits the scoping of actions. P[f] behaves exactly as P with actions renamed by the relabeling f. The term P|Q stands for the parallel composition of Pand Q according to an interleaving semantics with synchronized communication on complementary actions, resulting in the internal action τ . Again, time has to proceed equally on both sides of the operator, *i.e.*, deterministically. Finally, $\mu x. P$ denotes recursion which behaves as a distinguished solution to the equation x = P. The rules for action transitions are the same as for CCS, with the exception of the new clock-prefix operator and the rule for recursion; however, the former is identical to the one in Hennessy and Regan's TPL [15], and the latter is equivalent to the standard CCS rule over guarded terms [6].

The operational semantics for TACS^{LT} possesses several important properties [15]. Firstly, any process — but not every term — can perform a clock transition due to our adoption of a lazy nil–process **0** and a lazy prefix operator. This is referred to as the *laziness property* of TACS^{LT}; formally, $\forall P \in \mathcal{P}. \exists P' \in \mathcal{P}.$ $P \xrightarrow{\sigma} P'$. Secondly, the semantics is *time-deterministic*, *i.e.*, progress of time does not resolve choices. Formally, $P \xrightarrow{\sigma} P'$ and $P \xrightarrow{\sigma} P''$ implies $P' \equiv P''$, for all $P, P', P'' \in \widehat{\mathcal{P}}$, which can easily be proved via induction on the structure of P.

3. The Moller–Tofts Preorder

This section first recalls the faster-than preorder introduced by Moller and Tofts in [23], to which we refer as Moller-Tofts preorder, or MT-preorder for short. As the section's main contribution, we prove the compositionality of this preorder for arbitrary processes, which has only been conjectured by Moller and Tofts. Indeed, the compositionality proof offered in [23] is restricted to processes that do not have any parallel operators inside the scope of a recursion operator. The key for proving compositionality in the general setting is a nontrivial *commutation lemma* which considers what happens when adjacent action and clock transitions are transposed. This lemma also plays an important role when obtaining the full-abstraction result presented in Sec. 4 and when abstracting from internal computations in Sec. 7.

Definition 3.1 (MT-preorder [23]). A relation $\mathcal{R} \subseteq \mathcal{P} \times \mathcal{P}$ is an *MT-relation* if, for all $\langle P, Q \rangle \in \mathcal{R}$ and $\alpha \in \mathcal{A}$:

- (1) $P \xrightarrow{\alpha} P'$ implies $\exists Q', k, P''. Q \xrightarrow{\sigma} \stackrel{k}{\longrightarrow} Q', P' \xrightarrow{\sigma} \stackrel{k}{\longrightarrow} P''$, and $\langle P'', Q' \rangle \in \mathcal{R}$. (2) $Q \xrightarrow{\alpha} Q'$ implies $\exists P'. P \xrightarrow{\alpha} P'$ and $\langle P', Q' \rangle \in \mathcal{R}$. (3) $P \xrightarrow{\sigma} P'$ implies $\exists Q'. Q \xrightarrow{\sigma} Q'$ and $\langle P', Q' \rangle \in \mathcal{R}$.

- (4) $Q \xrightarrow{\sigma} Q'$ implies $\exists P'. P \xrightarrow{\sigma} P'$ and $\langle P', Q' \rangle \in \mathcal{R}$.

We write $P \sqsupseteq_{\mathrm{mt}} Q$ if $\langle P, Q \rangle \in \mathcal{R}$ for some MT–relation \mathcal{R} , and call $\sqsupseteq_{\mathrm{mt}}$ the MT– preorder.

Technically, all conditions of this definition, with the exception of the first one, are identical to the ones of temporal strong bisimulation (cf. [7,9]). Intuitively, the weaker first condition states that, if the faster process P can perform an action, then the slower process Q must not match this action right away, but can perform some arbitrary number k of time steps before doing so.² However, delaying k time steps may make the resulting process Q' faster than P'. To account for this, Moller and Tofts suggest that P' performs k time steps of its own, resulting in process P'' that should then be faster than Q'. To see the necessity for this, consider the processes $a.0|\sigma.b.0$ and $\sigma.a.0|\sigma.b.0$, for which a sensible faster-than preorder should clearly identify the former process as the faster one. Here, the *a*-transition of $a.0|\sigma.b.0$ to $0|\sigma.b.0$ can only be matched by the latter process after a delay of one time unit, leading to 0|b.0. However, $0|\sigma.b.0$ is not faster than 0|b.0, but only if it has delayed a time unit as well. The first condition of the MT-preorder *forces* the faster process to match the delay of the slower one. That it does so *immediately*, *i.e.*, within the same matching step, seems arbitrary and

²Note that we could have equally well quantified P'' universally in Def. 3.1(1), as P'' always exists and is uniquely determined due to the laziness property and the time-determinacy property of our semantics, respectively.

restrictive. Nevertheless, we will show in the next section that this is not the case and that there is a very natural explanation for this.

It is easy to see that \exists_{mt} is indeed a preorder, *i.e.*, it is reflexive and transitive, and that it is the largest MT–relation. Moreover, if one studies CCS process terms only, *i.e.*, TACS^{LT} processes not containing any clock prefix operator, then two processes are related in the MT–preorder if and only if they are strongly bisimilar. This is because all clock transitions are idling transitions in such a restricted setting, *i.e.*, σ –loops; vice versa, every process can idle due to the laziness property. Hence, CCS is a sub–calculus of TACS^{LT}.

Theorem 3.2 (Precongruence). The MT-preorder \exists_{mt} is a precongruence for all TACS^{LT} operators (including recursion).

The only difficult *and* non-standard part of the proof concerns compositionality regarding parallel composition, which is also needed for proving recursion compositional. The proof is based on the following novel *commutation lemma*, which does away with Moller and Tofts' unnecessary restrictions for the compositionality proof.

Lemma 3.3 (Commutation). Let $P, P' \in \mathcal{P}$ and $w \in (\mathcal{A} \cup \{\sigma\})^*$.

- (1) Simple commutation lemma: If $P \xrightarrow{w} \xrightarrow{\sigma} P'$, then $\exists P'' \cdot P \xrightarrow{\sigma} \xrightarrow{w} P''$ and $P' \rightrightarrows_{mt} P''$.
- (2) Commutation lemma: If $P \xrightarrow{w \to \sigma} {}^{k}P'$, for $k \in \mathbb{N}$, then $\exists P'' \cdot P \xrightarrow{\sigma} {}^{k} \xrightarrow{w} P''$ and $P' \underset{and}{\supseteq} {}^{mt}P''$.

Intuitively, the commutation lemma states that a delay, *i.e.*, one or more clock transitions, after a given sequence of transitions can also be made before this sequence. Moreover, and perhaps surprisingly, the earlier a delay is performed, the slower the resulting process is. One might expect that processes P' and P'' in the above lemma are equally fast. That this is not necessarily the case can be demonstrated by a simple example, taking $P =_{df} a.\sigma.b.\mathbf{0}$ and $w =_{df} a$. In this example, $P \xrightarrow{a} \xrightarrow{\sigma} b.\mathbf{0}$ and $P \xrightarrow{\sigma} P \xrightarrow{a} \sigma.b.\mathbf{0}$, where obviously $b.\mathbf{0}$ is strictly faster than $\sigma.b.\mathbf{0}$. This is because the "real" σ -transition after action a is traded against an *idling* or lazy σ -transition before a.

In the sequel we are mainly interested in Part (2) of the above lemma, which follows by induction on k and by employing Part (1). The proof of the simple commutation lemma is non-trivial and requires the introduction of some technical machinery. Before doing so we apply the lemma for proving the compositionality of the MT-preorder with respect to parallel composition.

Compositionality for parallel composition. According to Def. 3.1, it is sufficient to establish that $\mathcal{R} =_{df} \{ \langle P_1 | P_2, Q_1 | Q_2 \rangle | P_1 \beth_{mt} Q_1, P_2 \beth_{mt} Q_2 \}$ is an MT-relation. Let $\langle P_1 | P_2, Q_1 | Q_2 \rangle \in \mathcal{R}$ be arbitrary.

The only interesting case involves matching a transition $P_1|P_2 \xrightarrow{\alpha} P'_1|P'_2$, for some P'_1, P'_2 and some α , since all conditions except Cond. (1) of Def. 3.1 coincide with the standard ones for temporal strong bisimulation [7,9]. According to the operational rules for parallel composition we distinguish the following cases:

- $P_1 \xrightarrow{\alpha} P'_1$ and $P'_2 \equiv P_2$: Since $P_1 \beth_{mt} Q_1$ we know of the existence of some Q'_1, k, P''_1 such that $Q_1 \xrightarrow{\sigma}{\overset{k}{\longrightarrow}} Q'_1, P'_1 \xrightarrow{\sigma}{\overset{k}{\longrightarrow}} P''_1$, and $P''_1 \stackrel{\neg}{\underset{m}{\longrightarrow}} Q'_1$. Moreover, $P_2 \xrightarrow{\sigma} P_2''$ for some P_2'' , since every process is lazy and can thus engage in arbitrary delays. Because of $P_2 \beth_{\rm mt} Q_2$, there exists some Q_2' such that $Q_2 \xrightarrow{\sigma} {}^k Q'_2$ and $P''_2 \exists_{\mathrm{mt}} Q'_2$. Hence by our operational rules and the definition of \mathcal{R} , (i) $P'_1 | P'_2 \xrightarrow{\sigma} {}^k P''_1 | P''_2$, (ii) $Q_1 | Q_2 \xrightarrow{\sigma} {}^k \xrightarrow{\alpha} Q'_1 | Q'_2$, (iii) ⟨P''₁|P''₂, Q'₁|Q'₂⟩ ∈ R.
 P₂ → P'₂ and P'₁ ≡ P₁: This case is similar to the previous one.
- $\alpha = \tau$, $P_1 \xrightarrow{a} P'_1$, $P_2 \xrightarrow{\overline{a}} P'_2$, for some action $a \neq \tau$: Since $P_1 \gtrsim_{\rm mt} Q_1$ we know of the existence of some Q'_1, k, P''_1 such that $Q_1 \xrightarrow{\sigma}{k} \xrightarrow{a}{\longrightarrow} Q'_1$ $P'_1 \xrightarrow{\sigma}{}^k P''_1$, and $P''_1 \gtrsim_{\mathrm{mt}} Q'_1$. Similarly, since $P_2 \gtrsim_{\mathrm{mt}} Q_2$ we know of the existence of some Q'_2, l, P''_2 such that $Q_2 \xrightarrow{\sigma} \xrightarrow{l} \xrightarrow{\overline{\alpha}} Q'_2, P'_2 \xrightarrow{\sigma} P''_2$, and $P_2'' \beth_{\mathrm{mt}} Q_2'$. We distinguish the following cases:
 - $k = l: \text{ Here, } P_1'|P_2' \xrightarrow{\sigma}{}^k P_1''|P_2'' \text{ and } Q_1|Q_2 \xrightarrow{\sigma}{}^k \xrightarrow{\tau}{} Q_1'|Q_2'. \text{ More-over, } \langle P_1''|P_2'', Q_1'|Q_2' \rangle \in \mathcal{R} \text{ by the definition of } \mathcal{R}.$
 - $-k \neq l$: W.l.o.g. we assume k > l; the other case k < l is analogous. Moreover, we refer to the process between the clock transitions and the action transition on the path $Q_2 \xrightarrow{\sigma} \stackrel{l}{\longrightarrow} \stackrel{a}{\longrightarrow} Q'_2$ as \hat{Q}_2 . Due to the laziness property of processes and by Cond. 3 of Def. 3.1, there exists some \hat{P}_2'', \hat{Q}_2'' satisfying $P_2'' \xrightarrow{\sigma} \stackrel{k-l}{\longrightarrow} \hat{P}_2'', \hat{Q}_2 \xrightarrow{\overline{a}} Q'_2 \xrightarrow{\sigma} \stackrel{k-l}{\longrightarrow} Q'_2$, $\hat{Q}_2'', \text{ and } \hat{P}_2'' \xrightarrow{\neg}_{\mathrm{mt}} \hat{Q}_2''$. By Lemma 3.3(2) we know of the existence of some \hat{Q}_2' such that $\hat{Q}_2 \xrightarrow{\sigma} \stackrel{k-l}{\longrightarrow} \hat{Q}_2'$ and $\hat{Q}_2'' \xrightarrow{\neg}_{\mathrm{mt}} \hat{Q}_2'$. Hence, $P'_1|P'_2 \xrightarrow{\sigma} P''_1|\hat{P}''_2$ and $Q_1|Q_2 \xrightarrow{\sigma} \stackrel{\tau}{\longrightarrow} Q'_1|\hat{Q}'_2$ by our operational rules, and $\langle P''_1|\hat{P}''_2, Q'_1|\hat{Q}'_2\rangle \in \mathcal{R}$ by the definition of \mathcal{R} and the transitivity of \exists_{mt} .

This concludes the proof of compositionality.

The remainder of this section is devoted to establishing the correctness of the commutation lemma. While this exercise is quite technical, it sheds some light on the nature of faster-than preorders in the context of lower time bounds. We first define a simple syntactic faster-than relation on process terms that essentially encodes the syntactic implications of our intuition that any term P should be faster than σP .

Definition 3.4. The relation $\succ \subseteq \widehat{\mathcal{P}} \times \widehat{\mathcal{P}}$ is defined as the smallest relation satisfying the following properties, for all $P, P', Q, Q' \in \widehat{\mathcal{P}}$.

$$\begin{array}{cccc} \text{Always:} & (1) \ P \succ P & (2) \ P \succ \sigma.P \\ \text{If } P' \succ P \text{ and } Q' \succ Q \text{:} & (3) \ P'|Q' \succ P|Q & (4) \ P' + Q' \succ P + Q \\ & (5) \ P' \setminus L \succ P \setminus L & (6) \ P'[f] \succ P[f] \end{array}$$

If $P' \succ P$ and x guarded in $P \text{:} & (7) \ P'[\mu x. P/x] \succ \mu x. P$

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Observe that relation \succ is not transitive, *e.g.*, $P \succ \sigma.P$ and $\sigma.P \succ \sigma.\sigma.P$ but not $P \succ \sigma.\sigma.P$, and that it is also defined for open terms. It is interesting to note that \succ is carried over from [19], where we studied bisimulation-based fasterthan relations in the context of *upper* time bounds. The syntactic and semantic properties of \succ , relative to the process calculus TACS^{LT} considered in this article, are summarized in the following lemma.

Lemma 3.5. Let $P, P', Q, R \in \widehat{\mathcal{P}}, y \in \mathcal{V}$, and $\alpha \in \mathcal{A}$. Then:

- (1) $P \succ Q$ implies $P[R/y] \succ Q[R/y]$.
- (2) $P \xrightarrow{\sigma} P'$ implies $P' \succ P$.
- (3) $Q \succ P$ and $P \xrightarrow{\alpha} P'$ implies $\exists Q'. Q \xrightarrow{\alpha} Q'$ and $Q' \succ P'$.
- (4) $Q \succ P$ and $P \xrightarrow{\sigma} R$ implies $R \succ Q$.
- (5) $\succ_{\mid \mathcal{P} \times \mathcal{P}}$ is an MT-relation, whence $\succ_{\mid \mathcal{P} \times \mathcal{P}} \subseteq \exists_{mt}$

The most important part of this lemma is Part (5): if a process is syntactically faster than another according to \succ , then it is also semantically faster according to $\beth_{\rm mt}$. In this light, Part (2) shows that delaying processes indeed results in faster processes.

Proof. • *Part (1):* This statement is proved by induction on the inference length of $P \succ Q$, exactly as in [19]. The only interesting case concerns Case (7) of Def. 3.4, where we may assume $y \neq x$ since x is neither free in $P[\mu x.Q/x]$ nor in $\mu x.Q$, as well as $P[\mu x.Q/x] \succ \mu x.Q$ due to $P \succ Q$. Moreover, by Barendregt's Assumption, let us assume that there is no free occurrence of x in R. The induction hypothesis yields $P[R/y] \succ Q[R/y]$, whence $(P[\mu x.Q/x])[R/y] \equiv (P[R/y])[\mu x.(Q[R/y])/x] \succ \mu x.(Q[R/y]) \equiv (\mu x.Q)[R/y]$.

• Part (2): The proof of this statement is a straightforward induction on the structure of P.

• Part (3): The proof is by induction on the inference length of $P \succ Q$. The only interesting case concerns again Case (7) of Def. 3.4; note that Case (2) of Def. 3.4 is not applicable. Assume, $P' \succ P$ and $P \xrightarrow{\alpha} \hat{P}$ for some \hat{P} . Then we have $\mu x.P \xrightarrow{\alpha} \hat{P}[\mu x.P/x]$. By induction hypothesis, $P' \xrightarrow{\alpha} \hat{P}'$ for some $\hat{P}' \succ \hat{P}$. Hence, $P'[\mu x.P/x] \xrightarrow{\alpha} \hat{P}'[\mu x.P/x]$ and, by Part (1), $\hat{P}'[\mu x.P/x] \succ \hat{P}[\mu x.P/x]$.

• Part (4): The proof is again by induction on the inference length of $Q \succ P$. Note that Case (1) of Def. 3.4 is dealt with by Part (2). We only consider here Case (7) of Def. 3.4. Assume $P' \succ P$ and $P \xrightarrow{\sigma} \hat{P}$ for some \hat{P} . Then we have $\mu x.P \xrightarrow{\sigma} \hat{P}[\mu x.P/x]$. By induction hypothesis, $\hat{P} \succ P'$, whence $\hat{P}[\mu x.P/x] \succ P'[\mu x.P/x]$ by Part (1).

• Part (5): Consider arbitrary processes P, Q such that $P \succ Q$. According to Def. 3.1 we need to consider the following cases:

• $P \xrightarrow{\alpha} P'$: Due to the laziness property of our semantics regarding processes, but not necessarily terms, we know of the existence of some process Q' such that $Q \xrightarrow{\sigma} Q'$ and, by Lemma 3.5(4), $Q' \succ P$. When applying Lemma 3.5(3) we obtain some Q'' such that $Q' \xrightarrow{\alpha} Q''$ and $Q'' \succ P'$.

Since P' is a process as well, there is some P'' with $P' \xrightarrow{\sigma} P''$. Finally, by Lemma 3.5(4), $P'' \succ Q''$.

- $Q \xrightarrow{\alpha} Q'$: This case is dealt with by Lemma 3.5(3).
- $P \xrightarrow{\sigma} P'$: Since Q is a process, there is some Q' such that $Q \xrightarrow{\sigma} Q'$ and, by Lemma 3.5(4), $Q' \succ P$. Consequently, we must have $P' \succ Q'$ as well, according to the same Lemma 3.5(4).
- $Q \xrightarrow{\sigma} Q'$: Lemma 3.5(4) immediately yields $Q' \succ P$. Since P is a process, there exists some P' with $P \xrightarrow{\sigma} P'$ due to the laziness property of TACS^{LT}. Hence, $P' \succ Q'$ by Lemma 3.5(4), again.

This concludes the proofs of important properties of \succ .

With these prerequisites we can now prove the commutation lemma.

Proof. [of Lemma 3.3] • Part (1): Let $P, P_1, P' \in \mathcal{P}$ and $w \in (\mathcal{A} \cup \{\sigma\})^*$ such that $P \xrightarrow{w} P_1 \xrightarrow{\sigma} P'$. Because of Lemma 3.5(5), it is sufficient to establish the existence of some $P'', P_2 \in \mathcal{P}$ such that $P \xrightarrow{\sigma} P_2 \xrightarrow{w} P''$ and $P' \succ P''$. Since every process has a unique clock derivative due to time determinism and laziness, we know of the existence of a unique P_2 with $P \xrightarrow{\sigma} P_2$. According to Lemma 3.5(2), $P_2 \succ P$ holds. Further since $P \xrightarrow{w} P''$ and $P'' \succ P_1$ and because of Lemma 3.5(5), there exists some P'' such that $P_2 \xrightarrow{w} P''$ and $P'' \succ P_1$. Now, $P'' \succ P_1$ and $P_1 \xrightarrow{\sigma} P'$ yields $P' \succ P''$ by Lemma 3.5(4).

• Part (2): Let $P, P_1, P' \in \mathcal{P}, w \in (\mathcal{A} \cup \{\sigma\})^*$, and $k \in \mathbb{N}$ such that $P \xrightarrow{w} P_1 \xrightarrow{\sigma} P'$. The proof of Part (2) is by induction on k. For k = 0, the statement holds trivially. For k = 1, the statement is the one of Part (1). For the induction step, consider $P \xrightarrow{w} P_1 \xrightarrow{\sigma} P'_1 \xrightarrow{\sigma} P'$, for $k \ge 1$ and some $P'_1 \in \mathcal{P}$. By the induction hypothesis we know of the existence of some P_2, P'_2 such that $P \xrightarrow{\sigma} P'_2$. As the TACS^{LT} semantics for processes supports laziness, P'_2 can engage in a clock transition to some P''_2 , *i.e.*, $P'_2 \xrightarrow{\sigma} P''_2$. Because of $P'_1 \overrightarrow{\sim}_{mt} P'_2$ as well as time determinacy, we may conclude $P' \overrightarrow{\sim}_{mt} P''_2$. Applying the simple commutation lemma of Part (1) to $P_2 \xrightarrow{w} P'_2 \xrightarrow{\sigma} P''_2$, we obtain some P'' such that $P_2 \xrightarrow{\sigma} W P''_2$ and $P''_2 \overrightarrow{\sim}_{mt} P''$. Hence, $P \xrightarrow{\sigma} P''_2$, we obtain some P'' by the transitivity of $\overrightarrow{\sim}_{mt}$.

The next three sections of this article study the MT-preorder in detail. We will justify its motivation as a faster-than relation by means of formal theorems, and we will correct and generalize several statements made by Moller and Tofts in [23] concerning its semantic theory.

4. The MT–Preorder is Fully–Abstract

While the MT-preorder is algebraically appealing due to its precongruence property, it does not necessarily seem to be a natural choice for defining a fasterthan relation. As mentioned earlier, Def. 3.1 requires that differences in delays between processes must be accounted for within one step of matching, whence not all the future behavior of P' in Cond. (1) of Def. 3.1 is considered. In the following we explore an alternative *amortized* view of faster-than, where differences in delays can be smoothened out over several matching steps. The idea behind amortization is that processes performing delays later along execution paths are faster than functionally equivalent ones that perform delays earlier; this is because the former processes are executing actions at earlier absolute times — as measured from the start of the processes — than the latter ones.

As a simple example, consider the processes $P =_{df} a.b.\sigma.\sigma.c.0$ and $\sigma.a.\sigma.b.c.0$. In process P, actions a, b are executed at absolute time 0 and action c at absolute time 2. In process Q, analogously, action a is executed at absolute time 1 and actions b, c at absolute time 2. Hence, every action in P is executed earlier than, or at the same absolute time as in Q, whence P is strictly faster than Q. However, $P \not \gtrsim_{\mathrm{mt}} Q$ since the matching of P's *a*-transition requires Q to perform a delay of one time unit which cannot be saved as credit, but must be immediately spent by P, in this case in the form of idling. This enforced artificial idling is responsible that P is not deemed faster than Q in the framework of Moller and Tofts.

The following definition of an *amortized faster-than preorder* makes this idea of gaining and losing credit technically precise, using an index $i \in \mathbb{N}$ that keeps track of how many absolute time units the slower process is ahead of the faster one. Later on we will prove that the MT-preorder is fully-abstract with respect to this amortized preorder, which demonstrates that the MT-preorder has after all very intuitive roots.

Definition 4.1 (Amortized faster-than preorder). A family $(\mathcal{R}_i)_{i \in \mathbb{N}}$ of relations over \mathcal{P} is a family of faster-than relations if, for all $i \in \mathbb{N}$, $\langle P, Q \rangle \in \mathcal{R}_i$, and $\alpha \in \mathcal{A}$:

- (1) $P \xrightarrow{\alpha} P'$ implies $\exists Q', k. Q \xrightarrow{\sigma} \stackrel{k}{\longrightarrow} Q'$ and $\langle P', Q' \rangle \in \mathcal{R}_{i+k}$. (2) $Q \xrightarrow{\alpha} Q'$ implies $\exists P', k \leq i. P \xrightarrow{\sigma} \stackrel{k}{\longrightarrow} P'$ and $\langle P', Q' \rangle \in \mathcal{R}_{i-k}$.
- (2) $Q \longrightarrow Q'$ implies $\exists Q', k \ge 0. \ k \ge 1 i, \ Q \longrightarrow Q'$, and $\langle P', Q' \rangle \in \mathcal{R}_{i-1+k}$. (4) $Q \longrightarrow Q'$ implies $\exists P', k \ge 0. \ k \le i+1, \ P \longrightarrow P'$, and $\langle P', Q' \rangle \in \mathcal{R}_{i+1-k}$.

We write $P \sqsupseteq_i Q$ if $\langle P, Q \rangle \in \mathcal{R}_i$ for some family of faster-than relations $(\mathcal{R}_i)_{i \in \mathbb{N}}$, and call \beth_0 the amortized faster-than preorder.

This definition formalizes the intuition behind amortization as follows: $P \supseteq_i Q$ means that Q, or rather some predecessor of Q, has already performed *i* clock transitions that were not matched by P; therefore, P has a credit of i clock transitions that it might perform later without a match by Q (cf. Part (3) for k = 0). Any extra delays of the slower process when matching an action or clock transition of the faster process increase credit i accordingly (cf. Parts (1) and (3) for k > 1). Vice versa, an action or clock transition of the slower process does not necessarily have to be matched directly by the faster one: the latter may delay up to as many clock transitions as are allowed by the current credit i (cf. Parts (2) and (4)).

Another, more subtle example highlighting the difference between \beth_0 and the MT-preorder is exhibited by processes $P =_{df} c.a.\sigma.b.\mathbf{0} + c.a.b.\mathbf{0}$ and $Q =_{df} c.a.b.\mathbf{0}$.

The family $(\mathcal{R}_i)_{i\in\mathbb{N}}$ of faster-than relations defined by $\mathcal{R}_0 =_{\mathrm{df}} \{\langle P, Q \rangle\} \cup \{\langle R, R \rangle \mid$ $R \in \mathcal{P}\}, R_1 =_{\mathrm{df}} \{ \langle a.\sigma.b.\mathbf{0}, a.b.\mathbf{0} \rangle, \langle \sigma.b.\mathbf{0}, b.\mathbf{0} \rangle, \langle b.\mathbf{0}, b.\mathbf{0} \rangle, \langle \mathbf{0}, \mathbf{0} \rangle \} \text{ and } \mathcal{R}_i =_{\mathrm{df}} \emptyset, \text{ for } \mathcal{R}_i =_{\mathrm{df}} \emptyset$ i > 1, testifies to $P \sqsupseteq_0 Q$; note that $P \xrightarrow{c} a.\sigma.b.\mathbf{0}$ is matched by $Q \xrightarrow{\sigma} \xrightarrow{c} a.b.\mathbf{0}$ and $\langle a.\sigma.b.\mathbf{0}, a.b.\mathbf{0} \rangle \in \mathcal{R}_1$. However, we do not have $P \supseteq_{\mathrm{mt}} Q$. The step $P \xrightarrow{c} a.\sigma.b.\mathbf{0}$ could only be matched by $Q \xrightarrow{\sigma}{}^k \xrightarrow{c} a.b.\mathbf{0}$ for some $k \in \mathbb{N}$. Since $a.\sigma.b.\mathbf{0} \xrightarrow{\sigma}{}^k$ $a.\sigma.b.\mathbf{0}$, for any k, this would require $a.\sigma.b.\mathbf{0} \beth_{mt} a.b.\mathbf{0}$, which is clearly wrong.

It can be shown that the amortized faster-than preorder is indeed a preorder and that $(\beth_i)_{i \in \mathbb{N}}$ is the (componentwise) largest family of faster-than relations. However, there is an important shortcoming: \exists_0 is not preserved under parallel composition. Consider the processes P and Q above, where $P \sqsupseteq_0 Q$. For $R =_{df} \mu x.(\sigma.d.0 \mid \sigma.x)$, where d is a 'fresh' action not occurring in the sorts of P and Q, one may show that $P \mid R \not\geq_0 Q \mid R$ as follows: transition $P \mid R \xrightarrow{c} d$ $a.\sigma.b.\mathbf{0} \mid R$ would need to be matched by a sequence of transitions $Q \mid R \xrightarrow{\sigma}{k} \xrightarrow{c}{\longrightarrow}$ $a.b.0 | d.0 | \cdots | d.0 | R$, for some $k \in \mathbb{N}$ and k parallel components d.0, such that $a.\sigma.b.\mathbf{0} \mid R \supseteq_k a.b.\mathbf{0} \mid d.\mathbf{0} \mid \cdots \mid d.\mathbf{0} \mid R$ holds. Now, let the latter process engage in all d-computations of the k components d.0. Since d is a fresh action, these can only be matched by unfolding process R in $a.\sigma.b.0 \mid R$ k-times and by executing k clock transitions and k d-transitions. Thus, $a.\sigma.b.\mathbf{0} \mid R \gtrsim_0 a.b.\mathbf{0} \mid R$ would necessarily follow, *i.e.*, no credit remains. While the right-hand process can now engage in the sequence a.b, the left-hand process can only match action a, but not also action b due to the lack of credit.

To address this compositionality problem of \beth_0 we refine its definition.

Definition 4.2 (Amortized faster-than precongruence). A family $(\mathcal{R}_i)_{i \in \mathbb{N}}$ of relations over \mathcal{P} is a precongruence family if, for all $i \in \mathbb{N}$, $\langle P, Q \rangle \in \mathcal{R}_i$, and $\alpha \in \mathcal{A}$:

- (1) $P \xrightarrow{\alpha} P'$ implies $\exists Q', k, Q \xrightarrow{\sigma} \stackrel{k}{\longrightarrow} Q'$ and $\langle P', Q' \rangle \in \mathcal{R}_{i+k}$. (2) $Q \xrightarrow{\alpha} Q'$ implies $\exists P', k \leq i, P \xrightarrow{\sigma} \stackrel{k}{\longrightarrow} P'$ and $\langle P', Q' \rangle \in \mathcal{R}_{i-k}$.
- (3) $P \xrightarrow{\sigma} P'$ implies (a) i > 0 and $\langle P', Q \rangle \in \mathcal{R}_{i-1}$, or
 - (b) i = 0 and $\exists Q'. Q \xrightarrow{\sigma} Q'$ and $\langle P', Q' \rangle \in \mathcal{R}_i$.
- (4) $Q \xrightarrow{\sigma} Q'$ implies $\langle P, Q' \rangle \in \mathcal{R}_{i+1}$.

We write $P \supseteq_i Q$ if $\langle P, Q \rangle \in \mathcal{R}_i$ for some precongruence family $(\mathcal{R}_i)_{i \in \mathbb{N}}$ and call \supseteq_0 the amortized faster-than precongruence.

One can show that this amortized faster-than precongruence is indeed a preorder and that $(\exists_i)_{i \in \mathbb{N}}$ is the (componentwise) largest family of faster-than relations. This preorder's definition is identical to the one of the amortized fasterthan preorder, with the exception that a delay of the faster process now always results in consuming an available credit, while any delay of the slower process results in increasing the credit available to the faster one. As a consequence, it is easy to see that the amortized faster-than precongruence refines the amortized faster-than preorder, *i.e.*, $\exists_0 \subseteq \exists_0$. That this is indeed a proper inclusion can be seen by studying our example $c.a.\sigma.b.\mathbf{0} + c.a.b.\mathbf{0} \sqsupseteq_0 c.a.b.\mathbf{0}$. Again, $c.a.\sigma.b.\mathbf{0} + c.a.b.\mathbf{0} \xrightarrow{c} a.\sigma.b.\mathbf{0}$ can only be matched by $c.a.b.\mathbf{0} \xrightarrow{\sigma} \overset{k}{\longrightarrow} a.b.\mathbf{0}$

for some k, which demands $a.\sigma.b.\mathbf{0} \underset{\simeq}{\supseteq}_k a.b.\mathbf{0}$. Now $a.\sigma.b.\mathbf{0} \xrightarrow{\sigma}^k a.\sigma.b.\mathbf{0}$ requires $a.\sigma.b.\mathbf{0} \underset{\simeq}{\supseteq}_0 a.b.\mathbf{0}$ by Def. 4.2(1), which is obviously wrong since $a.b.\mathbf{0} \xrightarrow{ab}$ cannot be matched.

Theorem 4.3 (Coincidence). The preorders \beth_0 and \beth_{mt} coincide.

Proof. The inclusion $\exists_0 \subseteq \exists_{mt}$ follows immediately by the definitions of these preorders and the laziness property in TACS^{LT}; note that any credit the faster process might gain according to Def. 4.2 can immediately be removed via Rule (3). For establishing the other inclusion we prove that

$$\mathcal{R}_i =_{\mathrm{df}} \{ \langle P, Q \rangle \mid \exists \hat{P}. P \stackrel{\sigma}{\longrightarrow}^i \hat{P} \sqsupseteq_{\mathrm{mt}} Q \}$$

is a precongruence family (*cf.* Def. 4.2), whence $P \sqsupseteq_{\text{mt}} Q$ implies $\langle P, Q \rangle \in \mathcal{R}_0$. Let $\langle P, Q \rangle \in \mathcal{R}_i$ for some arbitrary *i*, *i.e.*, $P \xrightarrow{\sigma}{}^i \hat{P} \rightrightarrows_{\text{mt}} Q$. By Def. 4.2 we need to consider the following cases:

- $P \xrightarrow{\alpha} P'$: Because of the laziness and time-determinacy properties in TACS^{LT}, there is a unique P'' such that $P' \xrightarrow{\sigma} P''$. By Commutation Lemma 3.3(2) and by time determinacy, we obtain $\hat{P} \xrightarrow{\alpha} \hat{P}'$ for some \hat{P}' such that $P'' \stackrel{\neg}{\rightrightarrows}_{\mathrm{mt}} \hat{P}'$. Applying Def. 3.1(1) to $\hat{P} \stackrel{\neg}{\rightrightarrows}_{\mathrm{mt}} Q$ yields Q', k, \hat{P}'' satisfying $Q \xrightarrow{\sigma} \stackrel{k}{\longrightarrow} Q', \hat{P}' \xrightarrow{\sigma} \hat{P}''$, and $\hat{P}'' \stackrel{\neg}{\rightrightarrows}_{\mathrm{mt}} Q'$. Repeatedly applying Def. 3.1(4) to $P'' \stackrel{\neg}{\rightrightarrows}_{\mathrm{mt}} \hat{P}''$, proves the existence of some P''' such that $P'' \xrightarrow{\sigma} \hat{P}'''$ and $P''' \stackrel{\neg}{\rightrightarrows}_{\mathrm{mt}} \hat{P}''$. Hence, $P' \xrightarrow{\sigma} \hat{P}''' \stackrel{\neg}{\rightrightarrows}_{\mathrm{mt}} Q'$, *i.e.*, $\langle P', Q' \rangle \in \mathcal{R}_{i+k}$.
- $\langle P', Q' \rangle \in \mathcal{R}_{i+k}.$ • $Q \xrightarrow{\alpha} Q'$: We know by Def. 3.1(2) of some \hat{P}' such that $\hat{P} \xrightarrow{\alpha} \hat{P}'$ and $\hat{P}' \stackrel{\neg}{\rightarrow}_{\mathrm{mt}} Q'$. Hence, $P \xrightarrow{\sigma} \stackrel{i}{\longrightarrow} \hat{P}'$ and $\langle \hat{P}', Q' \rangle \in \mathcal{R}_0.$
- $P \xrightarrow{\sigma} P'$: If i > 0, then we obtain $\langle P', Q \rangle \in \mathcal{R}_{i-1}$ immediately. Otherwise $(i = 0), P \equiv \hat{P}, i.e., P \rightrightarrows_{\mathrm{mt}} Q$, whence establishing the existence of some Q' such that $Q \xrightarrow{\sigma} Q'$ and $P' \rightrightarrows_{\mathrm{mt}} Q'$. This implies $\langle P', Q' \rangle \in \mathcal{R}_0$, as desired.
- $Q \xrightarrow{\sigma} Q'$: Since $\hat{P} \beth_{\mathrm{mt}} Q$, there exists some \hat{P}' satisfying $\hat{P} \xrightarrow{\sigma} \hat{P}'$ and $\hat{P}' \beth_{\mathrm{mt}} Q'$. Hence, $P \xrightarrow{\sigma} \stackrel{i}{\longrightarrow} \hat{P}' \beth_{\mathrm{mt}} Q'$, *i.e.*, $\langle P, Q' \rangle \in \mathcal{R}_{i+1}$.

Hence, preorders \geq_0 and $\geq_{\rm mt}$ coincide.

Consequently, \exists_0 is not only a preorder but indeed a precongruence, since \exists_{mt} is a precongruence. Note, however, that the relations \exists_i , for i > 0, are not precongruences; for example, $\sigma.b.0 \exists_1 b.0$ but not $a.\sigma.b.0 \exists_1 a.b.0$ due to Def. 4.2(3).

Theorem 4.4 (Full abstraction). The preorder $\geq_0 = \geq_{mt}$ is the largest precongruence contained in \geq_0 .

Proof. By universal algebra, there exists a largest precongruence \exists_0^+ contained in \exists_0 , which is characterized by $\exists_0^+ = \{\langle P, Q \rangle | \forall \text{contexts } C[_]. \ C[P] \exists_0 \ C[Q] \}.$ Consequently, we are going to prove $\exists_{\text{mt}} = \exists_0^+$. We have already established that \exists_{mt} is a precongruence and, by Thm. 4.3, that $\exists_{mt} = \exists_0 \subseteq \exists_0$. Hence, $\exists_{mt} = \exists_{mt}^+ \subseteq \exists_0^+$. For proving the reverse inclusion $\exists_0^+ \subseteq \exists_{mt}$, it turns out to be convenient to define yet another characterization $\exists_{mt'}$ of \exists_{mt} and prove $\exists_0^+ \subseteq \exists_{mt'}$.

terization $\exists_{mt'}$ of \exists_{mt} and prove $\exists_0^+ \subseteq \exists_{mt'}$. The preorder $\exists_{mt'}$ is defined as \exists_{mt} , except for Cond. (3) in Def. 3.1 which is replaced by the following condition:

(3') $P \xrightarrow{\sigma} P'$ implies $\exists Q', P'', k \ge 1. Q \xrightarrow{\sigma} {}^{k}Q', P' \xrightarrow{\sigma} {}^{k-1}P'', \langle P'', Q' \rangle \in \mathcal{R}$.

This leads us to a notion of MT'-relation. First, observe that $\exists_{mt'} \equiv \exists_{mt}$. The inclusion " \supseteq " is trivial since Cond. (3') is less restrictive than Cond. (3). For proving the reverse inclusion " \subseteq " we show that $\exists_{mt'}$ is an MT-relation. This is trivial except for the case $P \xrightarrow{\sigma} P'$. In that case, due to the laziness property of TACS^{LT}, there exists a process Q' with $Q \xrightarrow{\sigma} Q'$. According to Cond. (4) and the time-determinacy property, $P' \rightrightarrows_{mt'} Q'$, as desired.

We may now establish the remaining inclusion $\beth_0^+ \subseteq \beth_{mt'} = \beth_{mt}$ by showing that

$$\mathcal{R}_a =_{\mathrm{df}} \{ \langle P, Q \rangle \mid C[P] \sqsupseteq_0 C[Q] \}$$

is an MT'-relation, where $C[_] =_{df} _|\mu x.(\sigma.d.\mathbf{0} | \sigma.x)$ for some 'fresh' action d that is not in the sorts of P and Q. Let $\langle P, Q \rangle \in \mathcal{R}_a$; according to Def. 3.1 we distinguish the following cases.

• $P \xrightarrow{\alpha} P'$: Hence, $C[P] \xrightarrow{\alpha} C[P']$ by the operational rules for TACS^{LT}. Since $C[P] \sqsupseteq_0 C[Q]$ and because of the definition of $C[_]$ we know of the existence of some Q', k such that

$$C[Q] \xrightarrow{\sigma}{}^{k} \xrightarrow{\alpha} C[Q'] \mid \underbrace{d.0 \mid \dots \mid d.0}_{k \text{ times}},$$

where $Q \xrightarrow{\sigma} \stackrel{k}{\longrightarrow} Q'$ and $C[P'] \rightrightarrows_k C[Q'] | d.0 | \dots | d.0$. Further, consider $C[Q'] | d.0 | \dots | d.0 \xrightarrow{d} C[Q']$.³ These action transitions must be matched by the faster process using exactly k clock transitions: according to the definition of $C[_]$ at least k and according to Cond. (2) at most k clock transitions. Hence, there exist processes $P_0, P_1, P_2, \dots, P_k$ and numbers j_1, j_2, \dots, j_k with $\sum_{1 \leq i \leq k} j_i = k$ such that, for $1 \leq i \leq k$, (i) $P_0 \equiv P'$, (ii) $C[P_{i-1}] \xrightarrow{\sigma} \stackrel{j_i}{\longrightarrow} \stackrel{d}{\longrightarrow} C[P_i]$, where $P_{i-1} \xrightarrow{\sigma} \stackrel{j_i}{\longrightarrow} P_i$, and (iii) $C[P_k] \rightrightarrows_0 C[Q']$. Thus, $\langle P_k, Q' \rangle \in \mathcal{R}_a$.

• $Q \xrightarrow{\alpha} Q'$: By TACS^{LT} semantics, $C[Q] \xrightarrow{\alpha} C[Q']$. Further, by Def. 4.1 and by the definition of $C[_]$, there exists some process P' such that $C[P] \xrightarrow{\alpha} C[P']$, where $P \xrightarrow{\alpha} P'$ and $C[P'] \sqsupseteq_0 C[Q']$. Thus, $\langle P', Q' \rangle \in \mathcal{R}_a$.

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³The latter process is really $C[Q'] | \mathbf{0} | \dots | \mathbf{0}$, but a parallel component **0** never makes any difference regarding the semantic preorders considered in this article; hence, we freely omit parallel components **0**.

- $P \xrightarrow{\sigma} P'$: Here, $C[P] \xrightarrow{\sigma} C[P'] \mid d.0$ by the operational rules of TACS^{LT}. $P \longrightarrow F$: here, $C[F] \longrightarrow C[F] | a.0$ by the operational rules of TRCS. Because of $C[P] \gtrsim_0 C[Q]$ and the definition of $C[_]$, there exists some Q' and some $k \ge 1$ such that $C[Q] \xrightarrow{\sigma}{}^k C[Q'] | d.0 | \cdots | d.0$ with k parallel com-ponents d.0, where $Q \xrightarrow{\sigma}{}^k Q'$, and $C[P'] | d.0 \gtrsim_{k-1} C[Q'] | d.0 | \cdots | d.0$. Because of the derivation $C[Q'] | d.\mathbf{0} | \cdots | d.\mathbf{0} \xrightarrow{d} C[Q']$ and since d is a fresh action not in the sort of P, we conclude that $C[P'] \mid d.0$ performs at least (cf. definition of C[.]) and at most (cf. Cond. (2)) k - 1 clock transitions and k d-transitions, giving $C[P''] \sqsupseteq_0 C[Q']$ for a process P''satisfying $P' \xrightarrow{\sigma} {}^{k-1} P''$. Hence, $\langle P'', Q' \rangle \in \mathcal{R}_a$, *i.e.*, Cond. (3') of the definition of $\exists_{mt'}$ holds.
- $Q \xrightarrow{\sigma} Q'$: In this situation we may derive $C[Q] \xrightarrow{\sigma} C[Q'] | d.0$, and one
 - of the following cases holds: $-k = 1, i.e., C[P] \xrightarrow{\sigma} C[P'] | d.0 \rightrightarrows_0 C[Q'] | d.0$: The *d*-transition of process C[Q'] | d.0 must be matched by the *d*-transition of C[P'] | d.0
 - such that $C[P'] \sqsupseteq_0 C[Q']$. $k = 0, i.e., C[P] \rightrightarrows_1 C[Q'] | d.0$: Here, the *d*-transition of C[Q'] | d.0can only be matched by a clock transition followed by a d-transition such that $C[P'] \sqsupseteq_0 C[Q']$.

In both cases we know of the existence of some P' such that $P \xrightarrow{\sigma} P'$ and $\langle P', Q' \rangle \in \mathcal{R}_a.$

This completes the full-abstraction proof.

 \square

Intuitively, Thms. 4.3 and 4.4 show that the MT-preorder rests on a very natural, amortized view of the notion of faster–than. Henceforth, we will call \beth_{mt} = \beth_0 the strong faster-than precongruence.

5. Axiomatizing the Moller–Tofts Preorder

We give a sound and complete axiomatization of the strong faster-than precongruence \beth_{mt} for the class of *finite* processes, *i.e.*, processes that do not contain any recursion operator. This allows us to compare our semantic theory for our calculus TACS^{LT} with lower time bounds, with the semantic theory that we developed in [19] for a calculus with upper time bounds, as well as with the CCS theory of strong bisimulation [21].

The axioms for the faster-than precongruence are shown in Table 3, where a term in square brackets is meant to be optional. Moreover, \sum is the indexed version of +, and we adopt the convention that the sum over the empty index set is identified with process 0. Any axiom of the form t = u should be read as two axioms $t \supseteq u$ and $u \supseteq t$. We write $\vdash t \supseteq u$ if $t \supseteq u$ can be derived from the axioms.

Axioms (A1)-(A4), (D1)-(D4), and (C1)-(C5) are exactly the ones for strong bisimulation in CCS [21]. Hence, the semantic theory of our calculus is distinguished from the one for strong bisimulation by the additional Axioms (P3)-(P6)

(A1)	t+u=u+t	(D1)	0 [f] = 0	
(A2)	t + (u+v) = (t+u) + v	(D2)	$(\alpha .t)[f] = f(\alpha).(t[f])$)
(A3)	t + t = t	(D3)	$(\sigma.t)[f] = \sigma.(t[f])$	
(A4)	t + 0 = t	(D4)	(t+u)[f] = t[f] + u[f]	
(P3)	$t + \sigma . t = t$	(C1)	$0 \setminus L = 0$	
(P4)	$\sigma.(t+u) = \sigma.t + \sigma.u$	(C2)	$(\alpha.t) \setminus L = 0$	$\alpha \in L \cup \overline{L}$
(P5)	$t \sqsupseteq \sigma.t$	(C3)	$(\alpha.t) \setminus L = \alpha.(t \setminus L)$	$\alpha \notin L \cup \overline{L}$
		(C4)	$(\sigma.t) \setminus L = \sigma.(t \setminus L)$	
(P6)	$\alpha.t = \alpha.\sigma.t + \alpha.t$	(C5)	$(t+u) \setminus L = (t \setminus L) + (t \cap L) + (t \cap L) + (t \cap L) + (t \cap L) + (t$	$u \setminus L$)
Let	$t \equiv \sum_{i \in I} \alpha_i t_i \left[+ \sigma t_\sigma \right]$ and	nd $u \equiv \sum$	$\sum_{j\in J}\beta_j.u_j \left[+\sigma.u_{\sigma}\right].$	
(E) $t u = \sum_{i \in I} \alpha_i \cdot (t_i u) + \sum_{j \in J} \beta_j \cdot (t u_j) + \sum_{\alpha_i = \overline{\beta}_i} \tau \cdot (t_i u_j) + \sum_{\alpha_i = \overline{\beta}_i} \tau \cdot (t_i u_i) + \sum_{\alpha_i \in \overline{\beta}_i} \tau \cdot (t_i u_i) + \sum_$				
	(0		if σt_{σ} and c	u_{σ} are absent
	$\int \sigma.(((\sum_{i\in I} \alpha_i t_i) + t_{\sigma})) (\sum_{i\in I} \alpha_i t_i) - t_{\sigma}) (\sum_{i\in I} \alpha_i t_i) - t_{\sigma})$	$\sum_{i \in J} \beta_j . u$	(u_j)) if only $\sigma . u_{\sigma}$	is absent
	$\int \sigma.((\sum_{i\in I} \alpha_i.t_i) ((\sum_{i\in J} \beta_i)) ((\sum_$	$(u_j) + i$	$(\iota_{\sigma}))$ if only σt_{σ}	is absent
	$\begin{cases} \sigma.(((\sum_{i\in I}\alpha_i.t_i) + t_{\sigma}) (\sum_{j\in I}\alpha_j.t_i) (\sum_{j\in I}\beta_j, \alpha_j.t_i) ((\sum_{j\in I}\alpha_j.t_i) + t_{\sigma}) ((\sum_{i\in I}\alpha_i.t_i) + t_{\sigma}) ((\sum_{i\in I}\alpha_j.t_i) + t_{\sigma}) ((\sum_{i\in I}\alpha_j.$	$\sum_{j\in J}^{}\beta_j.$	$u_j) + u_\sigma)$) otherwise	

TABLE 3. Axiom system for finite processes

and the refined *Expansion Law* (E). Further, it is distinguished from the one for the faster-than preorder for upper time bounds [19] by leaving out Axioms (P1) and (P2) related to enforcing upper time bounds, and by adding Axiom (P6). Intuitively, this added axiom states that inserting a delay within a path of a process does not alter the speed of the process, as long as there exists a functionally equivalent path without delay. This shows that our theory concentrates on bestcase behavior: the slower summand that has the optional delay can be ignored. Axiom (P6) generalizes to

(P6')
$$\alpha .t = \alpha . \sigma^k .t + \alpha .t$$
,

for any $k \in \mathbb{N}$, by repeated application; here, " σ^k ." stands for k nested clock prefixes. Axiom (P3) is similar in spirit to Axiom (P6) but cannot be derived. Axiom (P4) is a standard axiom in timed process algebras and testifies to the fact that time is a deterministic concept and does not resolve choices. Finally, Axiom (P5) encodes our elementary intuition of clock prefixes and speed within TACS^{LT}: any process t is faster than process $\sigma.t$ that must delay the execution of t by at least one clock tick.

5.1. Correctness

The correctness of our axioms relative to \exists_{mt} can be established as usual [21]. Note that all axioms, with the exception of Expansion Axiom (E) and Axiom (P3), are sound for arbitrary processes, not only for finite ones. For example, the correctness of Axiom (P5) follows from our syntactic relation \succ and Lemma 3.5(5). The correctness of direction " $\alpha t \supseteq \alpha . \sigma . t + \alpha . t$ " of Axiom (P6) is due to the correctness of Axioms (P5) and (A3). To prove the correctness of direction " $\alpha.\sigma.t + \alpha.t \supseteq \alpha.t$ ", the only interesting case is the matching of $\alpha.\sigma.t + \alpha.t \xrightarrow{\alpha} \sigma.t$. Here, we consider $\alpha.t \xrightarrow{\sigma} \alpha.t \xrightarrow{\alpha} t$, and observe $\sigma.t \xrightarrow{\sigma} t$ and $t \supseteq_{mt} t$. It should be noted that the axioms presented in [23] do not completely correspond with the MT-preorder, as has also been noted by Moller and Tofts since the publication of their paper in 1991 [*priv. commun.*]. For example, $a.\sigma.b.\mathbf{0} + a.b.\mathbf{0}$ is as fast as $a.b.\mathbf{0}$, which does not seem to be derivable from the axioms in [23]. In our theory, this example is a simple instantiation of Axiom (P6).

The only correctness proofs we provide in more detail concern the Expansion Axiom (E) and Axiom (P3). Moller and Tofts state in [23] that the "standard" expansion law [21] for faster-than relations based on lower time bounds does not hold, even for finite processes. While this observation is true for arbitrary processes, it is incorrect for finite ones. As a simple example we have $a.0 | \sigma.b.0 = a.(0|\sigma.b.0) + \sigma.(a.0|b.0)$, contrary to the claims in [23].

Correctness of Axiom (E) for finite processes. It suffices to consider the case $P \equiv \sum_{i \in I} \alpha_i . P_i + \sigma . P_{\sigma}$ and $Q \equiv \sum_{j \in J} \beta_j . Q_j + \sigma . Q_{\sigma}$. The other three cases are similar: in fact, the first case is obvious and coincides with the expansion axiom for CCS [21], while the second and third case can be derived from the fourth by considering $u_{\sigma} \equiv \mathbf{0}$ and $t_{\sigma} \equiv \mathbf{0}$, respectively. For notational convenience we simply abbreviate $\sum_{i \in I} \alpha_i . P_i$ by \sum_i and $\sum_{j \in J} \beta_j . Q_j$ by \sum_j . To prove the Expansion Axiom (E) correct, we show that

(i)
$$\mathcal{R} \cup \{ \langle P|Q, \sum_{i \in I} \alpha_i . (P_i|Q) + \sum_{j \in J} \beta_j . (P|Q_j) + \sum_{\alpha_i = \overline{\beta}_j} \tau . (P_i|Q_j) + \sigma . ((\sum_i + P_\sigma) \mid (\sum_j + Q_\sigma))) \} \cup \exists_{\mathrm{mt}} \text{ and}$$

(ii) $\mathcal{R}^{-1} \cup \{ \langle \sum_{i \in I} \alpha_i . (P_i|Q) + \sum_{j \in J} \beta_j . (P|Q_j) + \sum_{\alpha_i = \overline{\beta}_j} \tau . (P_i|Q_j) + \sigma . ((\sum_i + P_\sigma) \mid (\sum_j + Q_\sigma)), P|Q \rangle \} \cup \exists_{\mathrm{mt}}$

are MT-relations, where

$$\mathcal{R} = \{ \langle lhs, rhs \rangle \mid \exists k \in \mathbb{N}. \ P_{\sigma} \xrightarrow{\sigma}{}^{k} P_{\sigma}' \text{ and } Q_{\sigma} \xrightarrow{\sigma}{}^{k} Q_{\sigma}' \} \\ lhs = (\sum_{i} + P_{\sigma}') \mid (\sum_{j} + Q_{\sigma}') \\ rhs = \sum_{i \in I} \alpha_{i} \cdot (P_{i}|Q) + \sum_{j \in J} \beta_{j} \cdot (P|Q_{j}) + \sum_{\alpha_{i} = \overline{\beta}_{j}} \tau \cdot (P_{i}|Q_{j}) + \\ ((\sum_{i} + P_{\sigma}') \mid (\sum_{j} + Q_{\sigma}'))$$

We also implicitly exploit the correctness of Axiom (P6). Obviously, the action transitions of the left-hand side and of the right-hand side of Axiom (E) match, while the matching of a clock transition leads to the pair in \mathcal{R} for k = 0. Hence, it is sufficient to consider some arbitrary pair $\langle lhs, rhs \rangle \in \mathcal{R}$. Thus, there exists some $k \in \mathbb{N}$ such that $P_{\sigma} \xrightarrow{\sigma}{}^{k} P'_{\sigma}$ and $Q_{\sigma} \xrightarrow{\sigma}{}^{k} Q'_{\sigma}$.

For the proof of Claim (i), note that the action transitions of lhs are trivially matched by rhs. The converse is also the case for most action transitions, except for transitions $rhs \xrightarrow{\alpha_i} P_i | (\sum_{j \in J} \beta_j . Q_j + \sigma. Q_{\sigma})$, which deserve a closer look. These transitions can be matched by $lhs \xrightarrow{\alpha_i} P_i | (\sum_{j \in J} \beta_j . Q_j + Q'_{\sigma})$. Since $Q_{\sigma} \xrightarrow{\sigma} {}^k Q'_{\sigma}$ we know by Lemma 3.5 that $Q'_{\sigma} \gtrsim_{\mathrm{mt}} Q_{\sigma} \gtrsim_{\mathrm{mt}} \sigma. Q_{\sigma}$. Thus, $P_i | (\sum_{j \in J} \beta_j . Q_j + Q'_{\sigma}) \gtrsim_{\mathrm{mt}} P_i | (\sum_{j \in J} \beta_j . Q_j + \sigma. Q_{\sigma})$. Further, consider the clock transitions of lhs and rhs, *i.e.*, $lhs \xrightarrow{\sigma} lhs' \equiv (\sum_i + P''_{\sigma}) | (\sum_j + Q''_{\sigma})$ and $rhs \xrightarrow{\sigma} rhs' \equiv \sum_{i \in I} \alpha_i . (P_i | Q) + \sum_{j \in J} \beta_j . (P | Q_j) + \sum_{\alpha_i = \overline{\beta_j}} \tau. (P_i | Q_j) + (\sum_i + P''_{\sigma}) | \sum_j + Q''_{\sigma})$, for $P''_{\sigma}, Q''_{\sigma}$ satisfying $P'_{\sigma} \xrightarrow{\sigma} P''_{\sigma}$ and $Q'_{\sigma} \xrightarrow{\sigma} Q''_{\sigma}$. Since $P_{\sigma} \xrightarrow{\sigma} P''_{\sigma} \xrightarrow{\sigma} P''_{\sigma}$ and $Q_{\sigma} \xrightarrow{\sigma} {}^k Q'_{\sigma} \xrightarrow{\sigma} Q''_{\sigma}$ we have $\langle lhs', rhs' \rangle \in \mathcal{R}$.

For the proof of Claim (ii), the following property is essential:

 $\forall P \in \mathcal{P}_{\text{fin}}. \exists n \in \mathbb{N}. \forall P', P'' \in \mathcal{P}_{\text{fin}}. P \xrightarrow{\sigma} P', P' \xrightarrow{\sigma} P'' \text{ implies } P' \equiv P'' \qquad (*)$

This property can be established by induction on the structure of finite processes. When proving Claim (ii), observe that clock transitions and most action transitions can be dealt with as before. The interesting part of the proof is the matching of action transitions of the form $rhs \xrightarrow{\alpha_i} P_i \mid (\sum_{j \in J} \beta_j.Q_j + \sigma.Q_\sigma)$. We consider the transition sequence $lhs \xrightarrow{\sigma} \max^{\max+1} \xrightarrow{\alpha_i} P_i \mid (\sum_{j \in J} \beta_j.Q_j + \hat{Q}_\sigma)$, where max is the maximal number of clock transitions before process Q_σ starts idling, according to Property (*), and where \hat{Q}_σ is the unique process such that $Q_\sigma \xrightarrow{\sigma} \max^{\max} \hat{Q}_\sigma$ according to time determinacy. Further, $rhs \xrightarrow{\alpha_i} P_i \mid (\sum_{j \in J} \beta_j.Q_j + \sigma.Q_\sigma) \xrightarrow{\sigma} \max^{\max+1} P_i' \mid (\sum_{j \in J} \beta_j.Q_j + \hat{Q}_\sigma)$, where P_i' is the unique process satisfying $P_i \xrightarrow{\sigma} \max^{\max+1} P_i'$. Thus, $P_i' \sqsupseteq_{mt} P_i$ by Lemma 3.5 and $P_i' \mid (\sum_{j \in J} \beta_j.Q_j + \hat{Q}_\sigma) \rightrightarrows_{mt} P_i \mid (\sum_{j \in J} \beta_j.Q_j + \hat{Q}_\sigma)$ by Thm. 3.2, whence the Expansion Axiom is valid for finite processes.

The above proof relies on Property (*) that does not hold, *e.g.*, for the recursive process $D =_{df} \mu x.(d.\mathbf{0} \mid \sigma.x)$. When applying Axiom (E) to $a.\mathbf{0} \mid \sigma.D$ we obtain $a.\mathbf{0} \mid \sigma.D = a.(\mathbf{0} \mid \sigma.D) + \sigma.(a.\mathbf{0} \mid D)$. However, $a.(\mathbf{0} \mid \sigma.D) + \sigma.(a.\mathbf{0} \mid D) \not\gtrsim_{mt} a.\mathbf{0} \mid \sigma.D$. Assume otherwise; then, by Def. 3.1, the σ -derivatives of both processes must be related, *i.e.*, $a.(\mathbf{0} \mid \sigma.D) + (a.\mathbf{0} \mid D) \rightrightarrows_{mt} a.\mathbf{0} \mid D$ would hold. However, if the allegedly faster process performs an *a*-transition to $(\mathbf{0} \mid \sigma.D)$, then the slower process should match this after some delay of, say, $k \geq 1$ clock transitions; the case where k = 0 is obvious. According to Def. 3.1 we obtain

This is clearly invalid, as the slower process can engage in k consecutive d-transitions of which the faster process can only match the first k-1 transitions. Hence, Axiom (E) is not universally correct, which prohibits a straightforward extension of our axiomatization to larger classes of processes using standard techniques [20]. Future work shall investigate whether the Expansion Axiom holds for *sequential* processes, or even for processes in which recursion variables appear only at "sequential" positions.

Correctness of Axiom (P3) for finite processes. Direction " $t \supseteq t + \sigma t$ " can be derived from Axioms (P5) and (A3) and is thus correct for arbitrary processes. For establishing the correctness of the reverse direction we show that

$$\mathcal{R} =_{\mathrm{df}} \{ \langle P + Q, P \rangle \, | \, Q \xrightarrow{\sigma} P \text{ and } P \text{ satisfies Property } (*) \}$$

is an MT-relation. Thus, let $\langle P+Q, P \rangle \in \mathcal{R}$, and let max be number *n* of Property (*) for *P*; as mentioned earlier, every finite process satisfies this property.

The only interesting case arises when $P + Q \xrightarrow{\alpha} Q'$ due to $Q \xrightarrow{\alpha} Q'$, for some action α and finite process Q'. Because of the laziness property of TACS^{LT}, there exists some \hat{Q} such that $Q' \xrightarrow{\sigma} \operatorname{max}^{max+1} \hat{Q}$. We may then apply the commutation lemma, Lemma 3.3(2), to obtain \hat{P}, P' satisfying $Q \xrightarrow{\sigma} P \xrightarrow{\sigma} \operatorname{max}^{max} \hat{P}, \hat{P} \xrightarrow{\alpha} P'$, and $\hat{Q} \sqsupseteq_{\mathrm{mt}} P'$. Further, by the choice of max, we have $\hat{P} \xrightarrow{\sigma} \hat{P}$. Hence we have satisfied Def. 3.1(1): $P \xrightarrow{\sigma} \operatorname{max+1} \hat{P} \xrightarrow{\alpha} P', Q' \xrightarrow{\sigma} \operatorname{max+1} \hat{Q}$, and $\hat{Q} \rightrightarrows_{\mathrm{mt}} P'$. \Box To see that direction " \exists " of Axiom (P3) does not hold for arbitrary processes, it is sufficient to check that $D + \sigma.D \xrightarrow{\sigma} \operatorname{max} D$, where process D is defined as above. Consider the matching of transitions $D + \sigma.D \xrightarrow{\sigma}^2 (D \mid d.\mathbf{0} \mid d.\mathbf{0}) + (D \mid d.\mathbf{0}) \xrightarrow{d} D$ performed by the left-hand process, which forces the right-hand process to engage in transitions $D \xrightarrow{\sigma}^2 \xrightarrow{\sigma} \overset{k}{\to} d$ $D_{k+1} \equiv D \mid d.\mathbf{0} \mid \cdots \mid d.\mathbf{0}$, with k+1 components $d.\mathbf{0}$. Allowing the left-hand process to perform k further σ -transitions leads to $D \xrightarrow{\sigma} \overset{k}{\to} D_k$. But $D_k \not \supseteq_{\mathrm{mt}} D_{k+1}$, as the former process cannot match the k+1 d-transitions of the latter one.

5.2. Completeness

The completeness proof for our axiomatization is based on the following notion of *normal form*.

Definition 5.1 (Normal form). A finite process t is in normal form if

$$t \equiv \sum_{i \in I} \alpha_i . t_i \ \left[+ \sigma . t_\sigma \right],$$

where (i) I denotes a finite index set, (ii) $\alpha_i \in \mathcal{A}$ for all $i \in I$, (iii) all the t_i are in normal form, and (iv) the subterm in brackets is optional and, if it exists, t_{σ} is in normal form $\sum_{j \in J} \beta_j . u_j [+ \sigma . u_{\sigma}]$ and $\forall i \in I \exists j \in J$. $\alpha_i . t_i \equiv \beta_j . u_j$.

Observe that the unique clock derivative t' of a normal form t is again in normal form; its size is not larger than the size of t, and smaller if summand σt_{σ} is present in t. Further, $\vdash t' = t_{\sigma}$ by Cond. (iv) and a simple induction.

Proposition 5.2 (Rewriting into normal forms). For any finite process t, there exists some finite process u in normal form such that $\vdash t = u$.

The proof is by induction on the structure of finite processes and is quite straightforward. We only note here that Cond. (iv) of Def. 5.1 can be achieved by applying Axiom (P3). For proving our axiom system complete, the following technical lemmas are useful.

Lemma 5.3. Let $t \equiv \sum_{i \in I} \alpha_i t_i [+ \sigma t_\sigma]$ be in normal form, and let $t', u \in \mathcal{P}$ and $k \in \mathbb{N}$ such that $t \xrightarrow{\sigma} t'$ and $\vdash t' \supseteq u$. Then $\vdash t \supseteq \sigma^k u$.

Proof. The proof is by induction on k. For k = 0, *i.e.*, $t' \equiv t$, the statement is trivial. For k = 1 we have $t' \equiv \sum_{i \in I} \alpha_i . t_i [+ t_{\sigma}]$ by the operational rules for TACS^{LT}. Then, by repeated application of Axioms (P5) and (P4), $\vdash t = \sum_{i \in I} \alpha_i . t_i [+ \sigma . t_{\sigma}] \supseteq \sum_{i \in I} \sigma . \alpha_i . t_i [+ \sigma . t_{\sigma}] = \sigma . t' \supseteq \sigma . u$. For k > 1 we have $t \xrightarrow{\sigma} t'' \xrightarrow{\sigma} k' t'$, with k = k' + 1 and t'' being in normal form. Then, by induction, $\vdash t'' \supseteq \sigma^{k'} . u$. Similar reasoning to the case k = 1 yields $\vdash t \supseteq \sigma^{k'+1} . u = \sigma^k . u$. \Box

Lemma 5.4. Let $t \equiv \sum_{i \in I} \alpha_i . t_i + \sigma . t_\sigma$ be in normal form, $\gamma \in \mathcal{A} \cup \{\sigma\}$, $t' \in \mathcal{P}$ and $k \in \mathbb{N}$ such that $t \xrightarrow{\sigma}{k} \frac{\gamma}{\gamma} t'$. Then, there exists a sub-term $\gamma . t'$ of t with $\vdash t = t + \sigma^k . \gamma . t'$.

Proof. The statement is trivial for k = 0. If k > 0 we proceed by induction on k. For the induction base, k = 1, we have $t \xrightarrow{\sigma} t''$ for $t'' \equiv \sum_{i \in I} \alpha_i . t_i + t_{\sigma}$, where $t_{\sigma} \equiv \sum_{j \in J} \beta_j . u_j$ [$+ \sigma . u_{\sigma}$] satisfying $\forall i \in I \exists j \in J$. $\alpha_i . t_i \equiv \beta_j . u_j$ by Def. 5.1(iv). Hence, $\gamma . t' \equiv \beta_j . u_j$, for some $j \in J$. The desired property then holds simply by applying Axioms (A3) and (P4). Regarding the induction step, recall that the unique σ -derivative t'' of t is itself in normal form; a subterm $\gamma . t'$ of t'' is also a subterm of t, and obviously $\vdash t'' = t_{\sigma}$. Then, $\vdash t = t + \sigma . t_{\sigma} = t + \sigma . t'' = t + \sigma . (t'' + \sigma^k . \gamma . t') = t + \sigma . t'' + \sigma^{k+1} . \gamma . t' = t + \sigma^{k+1} . \gamma . t'$, as desired, where the third equality holds by induction hypothesis.

We are now able to state and prove the main result of this section.

Theorem 5.5 (Correctness & completeness). For finite processes t and u we have: $\vdash t \supseteq u$ if and only if $t \supseteq_{mt} u$.

Proof. The correctness " \Longrightarrow " of our axiom system follows by induction on the inference length of $\vdash t \sqsupseteq u$, as usual. We concentrate on proving completeness " \Leftarrow ". By Prop. 5.2 is suffices to prove this implication for processes t and u in normal form, *i.e.*, $t \equiv \sum_{i \in I} \alpha_i . t_i \ [+ \sigma . t_{\sigma} \]$ and $u \equiv \sum_{j \in J} \beta_i . u_i \ [+ \sigma . u_{\sigma} \]$. We proceed by induction on the sum of the process sizes of t and u. If this sum is zero we have $t \equiv u \equiv \mathbf{0}$, and we are done. Otherwise, we consider four cases, depending on whether each of the optional σ -summands $\sigma . t_{\sigma}$ and $\sigma . u_{\sigma}$ is present.

- Both summands σt_{σ} and σu_{σ} are absent: Hence, $t \equiv \sum_{i \in I} \alpha_i t_i$ and $u \equiv \sum_{j \in J} \beta_j u_j$. Due to $t \beth_{mt} u$ we may derive a couple of important properties:
 - (1) $\forall i \in I. \exists j \in J, k \in \mathbb{N}. \alpha_i = \beta_j, t_i \xrightarrow{\sigma}^k t' \text{ and } t' \exists_{mt} u_j (cf. \text{ Def. 3.1(1)}).$ The induction hypothesis yields $\vdash t' \sqsupseteq u_j$; recall that t' is in normal form. Hence by applying Lemma 5.3, $\vdash t_i \sqsupseteq \sigma^k.u_j$ which implies

 $\vdash \alpha_i . t_i \supseteq \beta_j . \sigma^k . u_j$. With Axiom (P6') we conclude $\vdash \beta_j . u_j + \alpha_i . t_i \supseteq \beta_j . u_j + \beta_j . \sigma^k . u_j = \beta_j . u_j$.

(2) $\forall j \in J. \exists i \in I. \ \beta_j = \alpha_i \text{ and } t_i \sqsupseteq_{\text{mt}} u_j \ (cf. \text{ Def. 3.1(2)}).$ By induction hypothesis, $\vdash t_i \sqsupseteq u_j \text{ holds, whence } \vdash \alpha_i.t_i \sqsupseteq \beta_j.u_j.$

We may now conclude this case as follows:

$$\begin{array}{l} (2, \text{A3}) & \vdash t = \sum_{i \in I} \alpha_i . t_i \\ (1) & \sqsupseteq \sum_{j \in J} \beta_j . u_j + \sum_{i \in I} \alpha_i . t_i \\ & \sqsupset \sum_{j \in J} \beta_j . u_j = u \end{array}$$

- Summand $\sigma.t_{\sigma}$ is present and $\sigma.u_{\sigma}$ absent: Because of $\sum_{i \in I} \alpha_i.t_i + \sigma.t_{\sigma} \gtrsim_{\mathrm{mt}} \sum_{j \in J} \beta_j.u_j$ we may derive the following properties similar to the previous case:
 - (1) $\forall i \in I. \exists j \in J. \vdash \beta_j.u_j + \alpha_i.t_i \sqsupseteq \beta_j.u_j.$
 - (2) $\forall j \in J. \exists i \in I. \vdash \alpha_i.t_i \sqsupseteq \beta_j.u_j.$
 - (3) When considering the initial clock transitions of t and u we obtain $t_{\sigma} \sqsupseteq_{\text{mt}} u$; note Cond. (iv) of Def. 5.1. Since t_{σ} is in normal form, the induction hypothesis applies and yields $\vdash t_{\sigma} \sqsupseteq u$.

We may now finish the case as follows:

$$\begin{array}{c} \vdash t = \sum_{i \in I} \alpha_i . t_i + \sigma . t_\sigma \\ \exists \sum_{j \in J} \beta_j . u_j + \sigma . t_\sigma + \sum_{i \in I} \alpha_i . t_i \\ (3) \qquad \qquad \exists u + \sigma . u + \sum_{i \in I} \alpha_i . t_i \\ (P3) \qquad \qquad = u + \sum_{i \in I} \alpha_i . t_i \\ (1) \qquad \qquad \exists u \end{array}$$

- Summand $\sigma.t_{\sigma}$ is absent and $\sigma.u_{\sigma}$ present: Here we have $\sum_{i \in I} \alpha_i.t_i \sqsupseteq_{\mathrm{mt}} \sum_{j \in J} \beta_j.u_j + \sigma.u_{\sigma}$. When considering a clock transition of both processes, Def. 3.1 implies $t \sqsupseteq_{\mathrm{mt}} \sum_{j \in J} \beta_j.u_j + u_{\sigma}$. Because the right-hand side process is in normal form, having a smaller size than the one of u, we may apply the induction hypothesis and Axiom (P5) in order to obtain $\vdash t \sqsupseteq \sum_{i \in J} \beta_j.u_j + u_{\sigma} \sqsupseteq \sum_{i \in J} \beta_j.u_j + \sigma.u_{\sigma} = u$.
- $\vdash t \supseteq \sum_{j \in J} \beta_j . u_j + u_{\sigma} \supseteq \sum_{j \in J} \beta_j . u_j + \sigma . u_{\sigma} = u.$ Both summands $\sigma . t_{\sigma}$ and $\sigma . u_{\sigma}$ are present: By the premise $\sum_{i \in I} \alpha_i . t_i + \sigma . t_{\sigma} \eqsim_{mt} \sum_{j \in J} \beta_j . u_j + \sigma . u_{\sigma}$ we may conclude the validity of the following properties, similar to the previous cases:
 - (1) By Def. 3.1(1) and by the induction hypothesis we have $\forall i \in I. \exists u', k$. $u \xrightarrow{\sigma} \stackrel{k}{\longrightarrow} \stackrel{\alpha_i}{\longrightarrow} u', t_i \xrightarrow{\sigma} \stackrel{k}{\longrightarrow} t'$, and $\vdash t' \sqsupseteq u'$. Lemma 5.3 yields $\vdash t_i \sqsupseteq$ $\sigma^k.u'$, whence $\vdash \alpha_i.t_i \sqsupseteq \alpha_i.\sigma^k.u' \sqsupseteq \sigma^k.\alpha_i.\sigma^k.u' = \sigma^k.\alpha_i.u'$ by Axioms (P5) and (P6'). We may now apply this to $\vdash u = u + \sigma^k.\alpha_i.\sigma^k.u'$, which follows from Lemma 5.4, in order to obtain $\vdash u + \alpha_i.t_i \sqsupseteq u$.
 - (2) $\forall j \in J. \exists i \in I. \vdash \alpha_i.t_i \sqsupseteq \beta_j.u_j.$
 - (3) $\vdash t_{\sigma} \supseteq u_{\sigma}$ (cf. Def. 3.1, Conds. (3) and (4)).

We may now finish this case as follows:

$$\begin{array}{ll} (2, A3) & \vdash t \sqsupseteq \sum_{j \in J} \beta_j . u_j + t \\ (3) & \sqsupseteq u + \sum_{i \in I} \alpha_i . t_i \\ (1) & \sqsupseteq u \end{array}$$

This completes the proof of Thm. 5.5.

6. Examples

This section applies our faster-than theory to two examples, exercising a classical bisimulation proof and an axiomatic proof, respectively.

6.1. Two-Place Storage

We first consider two implementations of a two-place storage in terms of *two* cells and a buffer, respectively. For simplifying the presentation we specify recursion via recursive process equations in the style of Milner [21], instead of using our recursion operator. The *two-cells* system is defined as the parallel composition of two one-place cells $C_0 \stackrel{\text{def}}{=} in.C_1$, where $C_1 \stackrel{\text{def}}{=} \sigma.\overline{out.}C_0$. The two-place buffer B_0 is given by the process equations $B_0 \stackrel{\text{def}}{=} in.B_1$, $B_1 \stackrel{\text{def}}{=} \sigma.\overline{out.}B_0 + in.B_2$ and $B_2 \stackrel{\text{def}}{=} \sigma.\overline{out.}B_1$. As is reflected by the σ -prefixes in front of the \overline{out} -actions, both cells C_0 and the two-place buffer B_0 have to delay at least one time unit before they can offer a communication on port \overline{out} . Intuitively, one would expect the two cell system to be strictly faster, since if both cells are full, then both of the stored data items may be output after a delay of only one time unit, while the buffer requires a delay of at least two time units until it may release the second data item.

As desired, our semantic theory for TACS^{LT} relates $C_0 | C_0$ and B_0 . Formally, this may be witnessed by the following MT-relation, in which we employ the abbreviations $C'_1 =_{df} \overline{out.} C_0$, $B'_1 =_{df} \overline{out.} B_0 + in.B_2$, and $B'_2 =_{df} \overline{out.} B_1$.

$$\{ \begin{array}{l} \langle C_0 | C_0, B_0 \rangle, & \langle C_1 | C_0, B_1 \rangle, & \langle C_0 | C_1, B_1 \rangle, & \langle C'_1 | C_0, B'_1 \rangle, \\ \langle C_0 | C'_1, B'_1 \rangle, & \langle C_1 | C_1, B_2 \rangle, & \langle C'_1 | C_1, B_2 \rangle, & \langle C_1 | C'_1, B_2 \rangle, \\ \langle C'_1 | C'_1, B'_2 \rangle, & \langle C'_1 | C_0, B_1 \rangle, & \langle C_0 | C'_1, B_1 \rangle \end{array} \}$$

It is easy to check, by referring to Def. 3.1, that this relation is indeed an MT– relation, whence $C_0 | C_0 \gtrsim_{\text{mt}} B_0$. Vice versa, $B_0 \gtrsim_{\text{mt}} C_0 | C_0$ does *not* hold according to Def. 3.1, since $C_0 | C_0$ can engage in the transition sequence $C_0 | C_0 \xrightarrow{in} \xrightarrow{in} \xrightarrow{\sigma} \xrightarrow{\overline{out} \ \overline{out}}$ which cannot be matched by B_0 . Thus, the two–cells system is strictly faster than the two–place buffer in all contexts, although functionally equivalent, which matches our intuition.

6.2. MAIL DELIVERY

The second example compares the speed of different forms of mail delivery and is adapted from [23]. Consider a fortunate nephew who has three uncles living overseas, all of whom send the nephew a selection of local newspapers *at best* every 14 days, once they got a nicely sized package of newspapers together. There are two kinds of delivery possible: the expensive *air mail* AM which takes at least 2 days to deliver and the cheap *surface mail* SM which takes a minimum of 10 days. Moreover, all three uncles come from different strata: the *rich uncle* RU can always afford the air-mail postage, while the *middle-class uncle* MU only sometimes can and the *poor uncle* PU never can. In TACS^{LT}, this situation can be modeled as follows:

	AM	$=_{\rm df}$ mail. σ^2 . deliver. 0	"air mail"
	SM	$=_{df}$ mail. σ^{10} . deliver. 0	"surface mail"
RU	$=_{\mathrm{df}}$	$\mu x.(\mathrm{AM} \sigma^{14}.x)$	"rich uncle"
MU	$=_{\mathrm{df}}$	$\mu x.((AM + SM) \sigma^{14}.x)$	" $middle-class~uncle$ "
PU	$=_{\rm df}$	$\mu x.(\mathrm{SM} \mid \sigma^{14}.x)$	"poor uncle"

Intuitively, our faster-than theory is concerned with best-case timing behavior, one would expect the process RU to be equally fast to MU, but MU to be strictly faster than PU.

To show this axiomatically we first note that $\vdash AM \sqsupseteq SM$ which can be obtained by applying Axiom (P5) eight times and that $\vdash AM = AM + SM$ by Axiom (P6'). Hence, $AM \sqsupseteq_{mt} SM$, $AM \sqsupseteq_{mt} AM + SM$, and $AM + SM \sqsupseteq_{mt} AM$ due to the correctness of the axioms (*cf.* Thm. 5.5). Because of the compositionality of \eqsim_{mt} with respect to parallel composition and recursion we may thus derive $RU \sqsupseteq_{mt} MU \sqsupseteq_{mt} PU$ and $MU \sqsupseteq_{mt} RU$. In addition, $PU \bowtie_{mt} MU$ since the trace mail. σ^2 . deliver of MU cannot be matched by PU according to Def. 3.1.⁴

7. Abstracting from Internal Computation

As usual in process algebra, one wishes to coarsen a semantic theory by abstracting from internal computation, *i.e.*, the unobservable action τ which is supposed to be hidden from an external observer. While doing so is usually quite straightforward for CCS-based calculi [21], it turns out to be highly non-trivial here; this may be the reason why it has not been attempted by Moller and Tofts in [23].

We start off by defining a weak version of our reference preorder, the amortized faster-than preorder, which requires us to introduce the following auxiliary notations. For any action α we define $\hat{\alpha} =_{df} \epsilon$, if $\alpha = \tau$, and $\hat{\alpha} =_{df} \alpha$, otherwise.

⁴Moller and Tofts incorrectly claim in their example that $AM + SM \not \supseteq_{mt} AM$ [23]. This contradicts the correctness of Axiom (P6); $AM + SM \not \supseteq_{mt} AM$ can be seen directly using Def. 3.1 when matching the only problematic transition $AM + SM \xrightarrow{mail} \sigma^{10}.$ deliver.0 by the transition sequence $AM \xrightarrow{\sigma} {}^{8}\underline{mail} \sigma^{2}.$ deliver.0 and by $\sigma^{10}.$ deliver.0 $\xrightarrow{\sigma} {}^{8}\sigma^{2}.$ deliver.0.

Further, we let $\stackrel{\epsilon}{\Longrightarrow} =_{\mathrm{df}} \stackrel{\tau}{\longrightarrow}^*$ and write $P \stackrel{\gamma}{\Longrightarrow} Q$, where $\gamma \in \mathcal{A} \cup \{\sigma\}$, if there exist R and S such that $P \stackrel{\epsilon}{\Longrightarrow} R \stackrel{\gamma}{\longrightarrow} S \stackrel{\epsilon}{\Longrightarrow} Q$. We also let $\stackrel{\sigma}{\Longrightarrow}^0$ stand for $\stackrel{\epsilon}{\Longrightarrow}$.

Definition 7.1 (Weak amortized faster-than preorder). A family $(\mathcal{R}_i)_{i \in \mathbb{N}}$ of relations over \mathcal{P} is a *family of weak faster-than relations* if, for all $i \in \mathbb{N}$, $\langle P, Q \rangle \in \mathcal{R}_i$, and $\alpha \in \mathcal{A}$:

- (1) $P \xrightarrow{\alpha} P'$ implies $\exists Q', k, k'. Q \xrightarrow{\sigma}^{k} \xrightarrow{\hat{\alpha}} \xrightarrow{\sigma}^{k'} Q'$ and $\langle P', Q' \rangle \in \mathcal{R}_{i+k+k'}$. (2) $Q \xrightarrow{\alpha} Q'$ implies $\exists P', k, k'. k+k' \leq i, P \xrightarrow{\sigma}^{k} \xrightarrow{\hat{\alpha}} \xrightarrow{\sigma}^{k'} P'$ and $\langle P', Q' \rangle \in \mathcal{R}_{i-k-k'}$.
- (3) $P \xrightarrow{\sigma} P'$ implies $\exists Q', k \ge 0. \ k \ge 1-i, \ Q \xrightarrow{\sigma} {}^k Q'$, and $\langle P', Q' \rangle \in \mathcal{R}_{i-1+k}$.
- (4) $Q \xrightarrow{\sigma} Q'$ implies $\exists P', k \ge 0. k \le i+1, P \xrightarrow{\sigma} P'$, and $\langle P', Q' \rangle \in \mathcal{R}_{i+1-k}$.

We write $P \stackrel{\neg}{\approx}_i Q$ if $\langle P, Q \rangle \in \mathcal{R}_i$ for a family of weak faster-than relations $(\mathcal{R}_i)_{i \in \mathbb{N}}$, and call $\stackrel{\neg}{\approx}_0$ the weak amortized faster-than preorder.

One can easily check that $(\exists_i)_{i \in \mathbb{N}}$ is the (componentwise) largest family of weak faster-than relations. Moreover, relation \exists_0 is indeed a preorder; while reflexivity is obvious, establishing transitivity is simple but not trivial. The best way of proving transitivity is by showing that $R_k =_{df} \{ \exists_i \circ \exists_j \mid i+j=k \}$, for $k \in \mathbb{N}$, is a family of weak faster-than relations. This can be done most elegantly by "diagram chasing" as in Fig. 1, drawing one diagram per condition of Def. 7.1. In each case, we take P, Q, R with $P \sqsupseteq_i Q \rightrightarrows_j R$ (dashed lines) and k = i + j, and we derive $\langle P, R \rangle \in R_{k'}$ for some suitable k' (dotted line).

Our weakening of the amortized faster-than preorder might appear surprising at first sight, due to the presence of $\stackrel{\sigma}{\Longrightarrow}^{k'}$ trailing weak action transitions on the right-hand side of the definition. As usual for weak bisimilarity, one may have a number of internal transitions before and after a matching action transition, and to get to these trailing internal transitions one may need to pass further clock transitions.

As in the strong case, it is easy to see that $\stackrel{\square}{\approx}_0$ is not a precongruence, even not for parallel composition. To identify the largest precongruence contained in $\stackrel{\square}{\approx}_0$, one may be tempted to first define a straightforward weak variant of the MT-preorder (with Cond. (3') as on page 14) and hope that this preorder is compositional for all operators except summation. The according definition would impose the following conditions on the notion of a weak MT-relation $\mathcal{R} \subseteq \mathcal{P} \times \mathcal{P}$, for $\langle P, Q \rangle \in \mathcal{R}$ and $\alpha \in \mathcal{A}$:

(1)
$$P \xrightarrow{\alpha} P'$$
 implies $\exists Q', k, P'', k' \colon Q \xrightarrow{\sigma}^{k} \xrightarrow{\hat{\alpha}} \xrightarrow{\sigma}^{k'} Q', P' \xrightarrow{\sigma}^{k+k'} P''$, and $\langle P'', Q' \rangle \in \mathcal{R}.$

(2)
$$Q \xrightarrow{\alpha} Q'$$
 implies $\exists P'. P \xrightarrow{\alpha} P'$ and $\langle P', Q' \rangle \in \mathcal{R}$.

(3)
$$P \xrightarrow{\sigma} P'$$
 implies $\exists Q', P'', k, Q \xrightarrow{\sigma} Q', P' \xrightarrow{\sigma} P''$, and $\langle P'', Q' \rangle \in \mathcal{R}$.
(4) $Q \xrightarrow{\sigma} Q'$ implies $\exists P', P \xrightarrow{\sigma} P'$ and $\langle P', Q' \rangle \in \mathcal{R}$.

Unfortunately, this preorder is not even included in $\stackrel{\square}{\approx}_0$, nor is it included in any other desirable weak faster-than preorder. The reason for this is that, *e.g.*,

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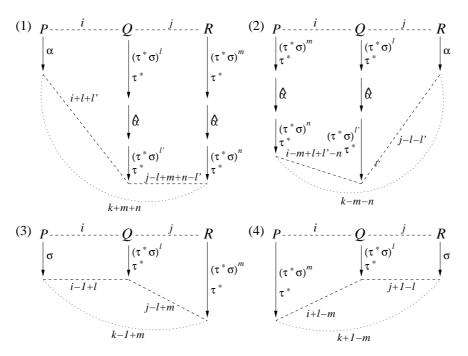


FIGURE 1. Diagrammatic transitivity proof.

 $\tau.(\tau.a.0 + \tau.b.0)$ would be deemed faster than a.0; in particular, the first τ transition of the allegedly faster process to $\tau .a.0 + \tau .b.0$ can be matched by $a.0 \xrightarrow{\sigma}$ $a.\mathbf{0}$ and choosing $\tau.a.\mathbf{0} + \tau.b.\mathbf{0} \xrightarrow{\tau} a.\mathbf{0} \xrightarrow{\sigma} a.\mathbf{0}$. However, $\tau.(\tau.a.\mathbf{0} + \tau.b.\mathbf{0}) \underset{\approx}{\mathbb{Z}} a.\mathbf{0}$, as the transition sequence $\tau.(\tau.a.\mathbf{0} + \tau.b.\mathbf{0}) \xrightarrow{\tau} \tau.a.\mathbf{0} + \tau.b.\mathbf{0} \xrightarrow{\tau} b.\mathbf{0} \xrightarrow{b} \mathbf{0}$ cannot be matched by process a.0. This example suggests one to demand, in Cond. (1), $P' \xrightarrow{\sigma}{}^{k+k'} P''$. Similarly, the example $\sigma.(\tau.a.\mathbf{0} + \tau.b.\mathbf{0})$ and $\sigma.\tau.a.\mathbf{0}$ shows that Cond. (3) should be modified to demand $P' \xrightarrow{\sigma}{}^{k-1} P''$. Furthermore, exploring compositionality for parallel composition implies also in Cond. (4) $P \xrightarrow{\sigma} P'$ (cf. proof of Prop. 7.6), which means that we may simply write $Q \xrightarrow{\sigma} Q'$ and $\langle P', Q' \rangle \in \mathcal{R}$ in Cond. (3) as well. This leads to the following definition of the $weak \ Moller-Tofts \ preorder.$

Definition 7.2 (Weak MT-preorder). A relation $\mathcal{R} \subseteq \mathcal{P} \times \mathcal{P}$ is a weak MTrelation if, for all $\langle P, Q \rangle \in \mathcal{R}$ and $\alpha \in \mathcal{A}$:

- (1) $P \xrightarrow{\alpha} P'$ implies $\exists Q', k, P'', k' \cdot Q \xrightarrow{\sigma}^{k} \xrightarrow{\hat{\alpha}} \xrightarrow{\sigma}^{k'} Q', P' \xrightarrow{\sigma}^{k+k'} P''$, and $\langle P'', Q' \rangle \in \mathcal{R}.$
- (2) $Q \xrightarrow{\alpha} Q'$ implies $\exists P'. P \xrightarrow{\hat{\alpha}} P'$ and $\langle P', Q' \rangle \in \mathcal{R}$. (3) $P \xrightarrow{\sigma} P'$ implies $\exists Q'. Q \xrightarrow{\sigma} Q'$ and $\langle P', Q' \rangle \in \mathcal{R}$.
- (4) $Q \xrightarrow{\sigma} Q'$ implies $\exists P'. P \xrightarrow{\sigma} P'$ and $\langle P', Q' \rangle \in \mathcal{R}$.

We write $P \stackrel{\square}{\approx}_{mt} Q$ if $\langle P, Q \rangle \in \mathcal{R}$ for some weak MT-relation \mathcal{R} , and call $\stackrel{\square}{\approx}_{mt}$ the weak MT-preorder.

We first show that $\stackrel{\neg}{\approx}_{mt}$ is a preorder. While reflexivity is obvious, it is difficult to see whether $\stackrel{\neg}{\approx}_{mt}$ is transitive, *i.e.*, whether $\stackrel{\neg}{\approx}_{mt} \circ \stackrel{\neg}{\approx}_{mt} \subseteq \stackrel{\neg}{\approx}_{mt}$ holds. In order to prove transitivity, we first note that $\stackrel{\neg}{\approx}_{mt}$ satisfies a property to which we refer as *quasi-transitivity*.

Lemma 7.3 (Quasi–Transitivity). $\exists_{mt} \circ \exists_{mt} \subseteq \exists_{mt}$

Proof. We show that $\exists_{mt} \circ \exists_{mt}$ is a weak MT–relation and restrict ourselves to the most interesting case of establishing Cond. (1) of Def. 7.2. Let *P*, *Q*, *R* such that $P \exists_{mt} Q$ and $Q \sqsupseteq_{mt} R$, and let $P \xrightarrow{\alpha} P'$ for some $\alpha \in \mathcal{A}$ and $P' \in \mathcal{P}$. Because of $P \exists_{mt} Q$ we may infer the existence of Q', Q'', k, P'' such that $Q \xrightarrow{\sigma} Q' \xrightarrow{\alpha} Q'', P' \xrightarrow{\sigma} P''$, and $P'' \exists_{mt} Q''$. Consequently, and by assumption $Q \rightrightarrows_{mt} R$, there exists process R' such that $R \xrightarrow{\sigma} R'$ and $Q' \rightrightarrows_{mt} R'$. According to Def. 7.2(1) we may further derive the existence of R'', l, l', Q''' satisfying $R' \xrightarrow{\sigma} Q' \xrightarrow{\alpha} P'', Q'' \xrightarrow{\sigma} Q'', Q'' \xrightarrow{\sigma} Q'', Q''' \xrightarrow{\sigma} Q'', Q''' \xrightarrow{\sigma} Q'', Q''' \xrightarrow{\sigma} Q'', Q''', Q''' \xrightarrow{\sigma} Q'', Q''', Q'' \xrightarrow{\sigma} Q'', Q''', Q''' \xrightarrow{\sigma} Q'', Q''', Q''' \xrightarrow{\sigma} Q'', Q'', Q''' \xrightarrow{\sigma} Q'', Q''', Q''' \xrightarrow{\sigma} Q'', Q''', Q''', Q''' \xrightarrow{\sigma} Q'', Q''', Q''', Q'' \xrightarrow{\sigma} Q'', Q''', Q''', Q''' \xrightarrow{\sigma} Q'', Q''', Q''',$

Next we establish an important technical lemma for which we need to introduce some notation. For $w, w' \in (\mathcal{A} \cup \{\sigma\})^*$ we write $w \equiv^{\mathrm{v}} w'$ if $w_{|\Lambda \cup \overline{\Lambda}} = w'_{|\Lambda \cup \overline{\Lambda}}$. Intuitively, $w \equiv^{\mathrm{v}} w'$ if the words w, w' are visibly equivalent, *i.e.*, if they are identical up to occurrences of σ and τ . We also let $|w|_{\sigma}$ denote the number of occurrences of σ in w.

Lemma 7.4. Let $Q, Q', R \in \mathcal{P}$ and $w \in (\mathcal{A} \cup \{\sigma\})^*$ with $Q \stackrel{\neg}{\approx}_{mt} R$ and $Q \stackrel{w}{\longrightarrow} Q'$. Then there exists some $Q'', R' \in \mathcal{P}, l \in \mathbb{N}$, and $w'' \in (\mathcal{A} \cup \{\sigma\})^*$ such that $w \equiv^v w''$, $|w''|_{\sigma} = |w|_{\sigma} + l, Q' \stackrel{\sigma}{\longrightarrow}^l Q'', R \stackrel{w''}{\longrightarrow} R'$, and $Q'' \stackrel{\neg}{\approx}_{mt} R'$.

Proof. The proof is by induction on the structure of word w. If $w = \epsilon$, then the statement holds trivially. If $w = \sigma v$ for some $v \in (\mathcal{A} \cup \{\sigma\})^*$, then one may easily prove the statement by referring to the induction hypothesis. Hence, we are left with the case $w = \alpha v$ for some $\alpha \in \mathcal{A}$. Thus, let process \hat{Q} be such that $Q \xrightarrow{\alpha} \hat{Q} \xrightarrow{v} Q'$. By Cond. (1) of Def. 7.2, there are processes R'', \hat{Q}' , a number \hat{l} , and a word w_{α} with $w_{\alpha} \equiv^{v} \alpha$, $|w_{\alpha}|_{\sigma} = \hat{l}$, $R \xrightarrow{w_{\alpha}} R'', \hat{Q} \xrightarrow{\sigma} \hat{l} \hat{Q}'$, and $\hat{Q}' \stackrel{\frown}{\underset{mt}{\rightrightarrows}} R''$. Due to the laziness property in TACS^{LT}, there exists some Q''' with $Q' \xrightarrow{\sigma} \hat{l} \hat{Q}' \xrightarrow{v} \hat{Q}'''$ and $Q''' \stackrel{\frown}{\underset{mt}{\rightrightarrows}} \hat{Q}'''$. We may now apply Lemma 3.3(2) to obtain a process \hat{Q}''' satisfying $\hat{Q} \xrightarrow{\sigma} \hat{l} \hat{Q}' \xrightarrow{v} \hat{Q}'''$ and $Q''' \stackrel{\frown}{\underset{mt}{\rightrightarrows}} \hat{Q}'''$. Applying the induction hypothesis to \hat{Q}', v, R'' yields processes \hat{Q}'', R' , a number l', and a word v' fulfilling the conditions $v \equiv^{v} v'$, $|v'|_{\sigma} = |v|_{\sigma} + l', \hat{Q}''' \xrightarrow{\sigma} l' \hat{Q}'', R'' \xrightarrow{v'} R'$, and $\hat{Q}'' \stackrel{\frown}{\underset{mt}{\rightrightarrows}} R''$. Since $Q''' \stackrel{\frown}{\underset{mt}{\rightrightarrows}} \hat{Q}''''$ and

 $\hat{Q}''' \xrightarrow{\sigma} \hat{Q}''$ we know by Cond. (4) of Def. 3.1 of the existence of some process Q'' such that $Q''' \xrightarrow{\sigma} \hat{Q}'' Q''$ and $Q'' \gtrsim_{\mathrm{mt}} \hat{Q}''$. Thus, $Q'' \gtrsim_{\mathrm{mt}} \hat{Q}'' \gtrsim_{\mathrm{mt}} R'$ and, by quasi-transitivity, $Q'' \gtrsim_{\mathrm{mt}} R'$. By setting $w'' =_{\mathrm{df}} w_{\alpha} v'$ and $l =_{\mathrm{df}} \hat{l} + l'$ we are done. \Box

Using this lemma we can now prove the transitivity of the weak MT-preorder.

Proof. (of property $\exists_{mt} \circ \exists_{mt} \subseteq \exists_{mt}$) It is sufficient to show that $\exists_{mt} \circ \exists_{mt}$ is a weak MT–relation. Let $P \sqsupseteq_{mt} Q \sqsupseteq_{mt} R$ for some processes P, Q, R. We focus only on Cond. (1) of Def. 7.2, since all other conditions are trivial to establish. Let $P \xrightarrow{\alpha} P'$, for which the premise $P \rightrightarrows_{mt} Q$ implies the existence of some Q', k, P'', k' such that $Q \xrightarrow{\sigma}^{k} \stackrel{\hat{\alpha}}{\Longrightarrow} \xrightarrow{\sigma}^{k'} Q', P' \xrightarrow{\sigma}^{k+k'} P''$, and $P'' \rightrightarrows_{mt} Q'$. Further, we apply Lemma 7.4 to obtain $w'' \in (A \cup \{\sigma\})^*, l \in \mathbb{N}, Q'' \in \mathcal{P}, \text{ and } R' \in \mathcal{P}$ such that $w'' \equiv^{\vee} \hat{\alpha}, |w''|_{\sigma} = k+k'+l, Q' \xrightarrow{\sigma}^{l} Q'', R \xrightarrow{w''} R'$, and $Q'' \rightrightarrows_{mt} R'$. Finally, Cond. (4) of Def. 7.2 guarantees the existence of some P''' such that $P'' \xrightarrow{\sigma}^{l} P'''$ and $P''' \rightrightarrows_{mt} Q''$. Hence, $R \xrightarrow{\sigma}^{l'} \hat{\alpha} \xrightarrow{\sigma}^{l''} R'$ for some $l', l'' \in \mathbb{N}$ with l'+l'' = k+k'+l, and $P''' \rightrightarrows_{mt} R'$. □

It is obvious from Defs. 3.1 and 7.2 that the MT–preorder \exists_{mt} is a weak MT–relation and thus included in the weak MT–preorder \exists_{mt} .

Lemma 7.5. \exists_{mt} is included in the weak amortized faster-than preorder \exists_{0} .

Proof. We prove that $\mathcal{R}_i =_{df} \{ \langle P, Q \rangle \mid P \xrightarrow{\sigma} {}^i P' \underset{\approx}{\supset} Q \}$, where $i \in \mathbb{N}$, is a family of weak faster-than relations. Let $\langle P, Q \rangle \in \mathcal{R}$, *i.e.*, $P \xrightarrow{\sigma} {}^i P'$ and $P' \underset{m}{\supseteq} Q$ for some $i \in \mathbb{N}$ and $P' \in \mathcal{P}$. The only interesting part of the proof concerns establishing Cond. 1 of Def. 7.2.

Accordingly, assume $P \xrightarrow{\alpha} P''$ for some $\alpha \in \mathcal{A}$ and $P'' \in \mathcal{P}$. Because of the laziness property of TACS^{LT}, there exists some P_1 such that $P'' \xrightarrow{\sigma} P_1$. Applying Commutation Lemma 3.3(2) yields a process P_2 satisfying $P' \xrightarrow{\alpha} P_2$ and $P_1 \stackrel{\sim}{\underset{mt}{\sim}} P_2$. Further, because of $P' \stackrel{\sim}{\underset{mt}{\cong}} Q$ we know of the existence of Q', k, k', P_3 such that $Q \xrightarrow{\sigma} \stackrel{k}{\Longrightarrow} \stackrel{\alpha}{\Longrightarrow} \stackrel{\sigma}{\Longrightarrow} \stackrel{k'}{Q'}, P_2 \xrightarrow{\sigma} \stackrel{k+k'}{\longrightarrow} P_3$, and $P_3 \stackrel{\sim}{\underset{mt}{\cong}} Q'$. Moreover, Def. 3.1(4) implies $P_1 \xrightarrow{\sigma} \stackrel{k+k'}{\longrightarrow} P_4$ for some $P_4 \in \mathcal{P}$ with $P_4 \stackrel{\sim}{\underset{mt}{\cong}} P_3$. Hence, $P'' \xrightarrow{\sigma} \stackrel{i+k+k'}{\longrightarrow} P_4$ and $P_4 \stackrel{\sim}{\underset{mt}{\cong}} P_3 \stackrel{\sim}{\underset{mt}{\cong}} Q'$. By quasi-transitivity (*cf.* Lemma 7.3) and the definition of \mathcal{R} we may now conclude $\langle P'', Q' \rangle \in \mathcal{R}_{i+k+k'}$, as desired. \Box

The weak MT-preorder is not only a preorder but also a precongruence.

Proposition 7.6. The weak MT-preorder $\exists_{\approx mt}$ is compositional for all TACS^{LT} operators except for the summation operator.

Proof. We restrict ourselves to the most interesting case of verifying compositionality of $\stackrel{\neg}{\approx}_{mt}$ with respect to parallel composition. To do so we show that $\mathcal{R} =_{df} \{ \langle P_1 | P_2, Q_1 | Q_2 \rangle | P_1 \stackrel{\neg}{\approx}_{mt} P_2, Q_1 \stackrel{\neg}{\approx}_{mt} Q_2 \}$ is a weak MT–relation.

Let $\langle P_1 | P_2, Q_1 | Q_2 \rangle \in \mathcal{R}$ be arbitrary. The only difficult part of the proof concerns establishing Cond. (1) of Def. 7.2 in the case of synchronization. Let For the content of t $P_2 \stackrel{\square}{\approx}_{\mathrm{mt}} Q_2$ we know of the existence of some Q'_2, l, P''_2, l' such that $Q_2 \stackrel{\sigma}{\Longrightarrow} \stackrel{l}{\Longrightarrow}$ $\stackrel{\sigma}{\Longrightarrow} \stackrel{l'}{Q'_2}, P'_2 \stackrel{\sigma}{\longrightarrow} \stackrel{l+l'}{\longrightarrow} P''_2, \text{ and } P''_2 \stackrel{\Box}{\approx}_{mt} Q'_2. We distinguish the following cases:$

- k = l: W.l.o.g. we further assume $k' \ge l'$. Due to the laziness property in TACS^{LT} there exists some Q''_2 with $Q'_2 \xrightarrow{\sigma} k'^{-l'} Q''_2$ and, because of $P''_2 \gtrsim_{\mathrm{mt}} Q'_2$, there exists some \hat{P}''_2 such that $P''_2 \xrightarrow{\sigma} k'^{-l'} \hat{P}''_2$ and $\hat{P}''_2 \gtrsim_{\mathrm{mt}} Q''_2$. Then, $Q_1 | Q_2 \xrightarrow{\sigma} k \xrightarrow{\sigma} k' Q'_1 | Q''_2$ and $P'_1 | P'_2 \xrightarrow{\sigma} k^{k+k'} P''_1 | \hat{P}''_2$ by our operational rules, and $\langle P_1''|\hat{P}_2'', Q_1'|Q_2''\rangle \in \mathcal{R}$ by the definition of \mathcal{R} .
- $k \neq l$: W.l.o.g. we assume k > l. We refer to the process between the weak clock transitions and the weak action transition on the path $Q_2 \stackrel{\sigma}{\Longrightarrow} \stackrel{l}{\Longrightarrow} \stackrel{\overline{a}}{\Longrightarrow} \stackrel{\sigma}{\Longrightarrow} \stackrel{l'}{Q'_2} Q'_2$ as \hat{Q}_2 . Because of the laziness property in TACS^{LT} and since $P_2'' \stackrel{\square}{\approx}_{\text{mt}} Q_2'$, there exist processes \hat{P}_2'', \hat{Q}_2' satisfying $P_2'' \stackrel{\sigma}{\longrightarrow}^{k-l} \hat{P}_2''$ $Q'_2 \xrightarrow{\sigma} {}^{k-l} \hat{Q}'_2$, and $\hat{P}''_2 \gtrsim_{\text{mt}} \hat{Q}'_2$. (This is the place in this proof we referred to in the last few lines before Definition 7.2.) We may now apply Lemma 3.3(2) and Def. 3.1(3) to obtain some \hat{Q}_2'' such that $\hat{Q}_2 \xrightarrow{\sigma} {}^{k-\bar{l}} \xrightarrow{\bar{a}}$ $\stackrel{\sigma}{\Longrightarrow}^{l'} \hat{Q}_2'' \text{ and } \hat{Q}_2' \beth_{\mathrm{mt}} \hat{Q}_2'' \text{ . Now, } \hat{P}_2'' \beth_{\mathrm{mt}} \hat{Q}_2' \beth_{\mathrm{mt}} \hat{Q}_2'', \text{ whence } \hat{P}_2'' \beth_{\mathrm{mt}} \hat{Q}_2''$ because of $\beth_{\mathrm{mt}} \subseteq \beth_{\mathrm{mt}}$ and the transitivity of \beth_{mt} . Now we are in the case k = l.

This concludes the compositionality proof of \gtrless_{mt} .

As expected for a CCS-based process calculus,
$$\gtrless_{mt}$$
 is not a precongruence for the summation operator, but the summation fix used for other bisimulation-based timed process algebras [9] proves effective for TACS^{LT}, too.

Definition 7.7 (Weak MT-precongruence). A relation $\mathcal{R} \subseteq \mathcal{P} \times \mathcal{P}$ is a *weak MT*-precongruence relation if, for all $\langle P, Q \rangle \in \mathcal{R}$ and $\alpha \in \mathcal{A}$:

- (1) $P \xrightarrow{\alpha} P'$ implies $\exists Q', k, P'', k' \cdot Q \xrightarrow{\sigma}^{k} \xrightarrow{\alpha} \xrightarrow{\sigma}^{k'} Q', P' \xrightarrow{\sigma}^{k+k'} P''$, and $P'' \stackrel{\boxtimes}{\approx}_{mt} Q'.$ (2) $Q \xrightarrow{\alpha} Q'$ implies $\exists P' \cdot P \xrightarrow{\alpha} P'$ and $P' \stackrel{\boxtimes}{\approx}_{mt} Q'.$
- (3) $P \xrightarrow{\sigma} P'$ implies $\exists Q'. Q \xrightarrow{\sigma} Q'$ and $\langle P', Q' \rangle \in \mathcal{R}$.
- (4) $Q \xrightarrow{\sigma} Q'$ implies $\exists P'. P \xrightarrow{\sigma} P'$ and $\langle P', Q' \rangle \in \mathcal{R}$.

We write $P \underset{\cong}{\rightrightarrows} Q$ if $\langle P, Q \rangle \in \mathcal{R}$ for some weak MT-precongruence relation \mathcal{R} , and call $\stackrel{\square}{\approx}_{mt}$ the weak MT-precongruence.

Again, $\stackrel{\neg}{\underset{m_{t}}{\boxtimes}}$ is a preorder and the largest weak MT–precongruence relation. It is worth pointing out that the strong faster–than precongruence $\beth_{\rm mt}$ is contained in

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the weak faster-than precongruence $\stackrel{\neg}{\approx}_{mt}$, which follows by inspecting the respective definitions. The recursive definition of the weak MT–precongruence employed in Conds. (3) and (4) above reflects the fact that clock transitions do not resolve choices [9].

Theorem 7.8. $\stackrel{\square}{\cong}_{mt}$ is the largest precongruence contained in $\stackrel{\square}{\approx}_{mt}$.

Proof. The proof of compositionality of this preorder regarding the TACS^{LT} operators is quite standard, except for the parallel composition operator that needs to be treated as for the weak MT–preorder before. Containment is proved by showing that $\stackrel{\triangleleft}{\approx}_{mt} \cup \stackrel{\triangleleft}{\approx}_{mt}$ is a weak MT–relation.

be treated as for the weak MT-preorder before. Containment is proved by showing that $\overrightarrow{\boxtimes}_{mt} \cup \overrightarrow{\boxtimes}_{mt}$ is a weak MT-relation. We are left with establishing the "largest" claim. From universal algebra we know that the *largest* precongruence $\overrightarrow{\boxtimes}_{mt}^+$ in $\overrightarrow{\boxtimes}_{mt}$ exists and also that $\overrightarrow{\boxtimes}_{mt}^+ = \{\langle P, Q \rangle | \forall C[x], C[P] \overrightarrow{\boxtimes}_{mt} C[Q]\}$. Since $\overrightarrow{\boxtimes}_{mt}$ is a precongruence that is contained in $\overrightarrow{\boxtimes}_{mt}$, the inclusion $\overrightarrow{\boxtimes}_{mt} \subseteq \overrightarrow{\boxtimes}_{mt}^+$ holds. Thus, it remains to show $\overrightarrow{\boxtimes}_{mt}^+ \subseteq \overrightarrow{\boxtimes}_{mt}$. Consider the relation $\overrightarrow{\boxtimes}_{mt} =_{df} \{\langle P, Q \rangle | P + c.0 \overrightarrow{\boxtimes}_{mt} Q + c.0$, where *c* is not in the sorts of *P*, *Q*}. By definition of $\overrightarrow{\boxtimes}_{mt}^{aux}$ we have $\overrightarrow{\boxtimes}_{mt}^+ \subseteq \overrightarrow{\boxtimes}_{mt}^{aux}$. We establish the other inclusion $\overrightarrow{\boxtimes}_{mt} \subseteq \overrightarrow{\boxtimes}_{mt}$ by proving that $\overrightarrow{\boxtimes}_{mt}^{aux}$ is a weak MT-precongruence relation. Let $P \overrightarrow{\boxtimes}_{mt}^{aux} Q$, *i.e.*, $P + c.0 \overrightarrow{\boxtimes}_{mt} Q + c.0$, and distinguish the following cases.

- Action transitions: Let $P \xrightarrow{\alpha} P'$, *i.e.*, $\alpha \neq c$ and $P + c.\mathbf{0} \xrightarrow{\alpha} P'$ by Rule (Sum1). Since $P \stackrel{\supseteq}{\underset{mt}{\cong}}^{aux} Q$ we know of the existence of some processes R, P'' and $k, k' \in \mathbb{N}$ satisfying $Q + c.\mathbf{0} \stackrel{\sigma}{\Longrightarrow}^{k} \stackrel{\hat{\alpha}}{\Longrightarrow} \stackrel{\sigma}{\Longrightarrow}^{k'} R, P' \stackrel{\sigma}{\longrightarrow}^{k+k'} P''$ and $P'' \stackrel{\supseteq}{\underset{mt}{\boxtimes}} R$. Since P'' cannot perform a c-transition, $Q + c.\mathbf{0}$ must have performed some action from Q to become R; we conclude $Q \stackrel{\sigma}{\longrightarrow} \stackrel{l}{\xrightarrow{\alpha}} \stackrel{\sigma}{\longrightarrow} \stackrel{l'}{R} R$ with l+l' = k+k'. The reverse case, where process Q engages in an action transition, is straightforward, as Cond. (2) of Defs. 7.2 and 7.7 coincides with the one for observation equivalence and observation congruence in CCS [21].
- Clock transitions: Let $P \xrightarrow{\sigma} P'$. By Rules (tAct) and (tSum), $P+c.0 \xrightarrow{\sigma} P'+c.0$ holds. Since $P \underset{mt}{\supseteq}^{aux} Q$ we know of the existence of some process R such that $Q + c.0 \xrightarrow{\sigma} R$ and $P' + c.0 \underset{mt}{\supseteq} R$. As clock derivatives are unique we have $R \equiv Q' + c.0$ for some Q' satisfying $Q \xrightarrow{\sigma} Q'$. Because c is a distinguished action not in the sorts of P' and Q' we may further conclude $P' \underset{mt}{\supseteq}^{aux} Q'$, as desired. The other case, where process Q engages in a clock transition, is analogous.

This shows that $\stackrel{\neg}{\cong}_{mt}^{aux}$ is a weak MT–precongruence relation. Hence, $\stackrel{\neg}{\cong}_{mt}^{aux} \subseteq \stackrel{\neg}{\cong}_{mt}^{aux}$, as desired.

It remains an open question whether the weak MT–precongruence is also the largest precongruence contained in the weak amortized faster–than preorder.

8. Related Work

Although there is a wealth of literature on timed process algebras [7], little work has been done in developing theories for relating processes with respect to speed. The approaches closest to ours are obviously the one by Moller and Tofts regarding processes equipped with lower time bounds [23], and our own one regarding processes equipped with upper time bounds [19]. As these have been referred to and discussed throughout, we refrain from repetitions here.

The probably best-known related work focuses on comparing process efficiency rather than process speed. Arun-Kumar and Hennessy [4, 5] have developed a bisimulation-based theory for untimed processes that is based on counting internal actions, which was later carried over to De Nicola and Hennessy's testing framework [13] by Natarajan and Cleaveland [24]. In these theories, runs of parallel processes are seen to be interleaved runs of their component processes. Consequently, e.g., $(\tau .a.0 | \tau . \overline{a}.b.0) \setminus \{a\}$ is as efficient as $\tau . \tau . \tau . b.0$, whereas, in our setting, $(\sigma .a.0 | \sigma . \overline{a}.b.0) \setminus \{a\}$ is strictly faster than $\sigma . \sigma . \tau . b.0$.

The sparse work on comparing process speeds largely concentrated on worstcase timing behavior on the basis of upper time bounds. Research by Vogler *et al.* [18, 26] originally employed the concurrency-theoretic framework of Petri nets and testing semantics [13]; it has only recently been carried over to a Theoretical– CSP-style [25] process algebra, called PAFAS [12,17]. The justification for adopting a testing approach is reflected in a fundamental result stating that the considered faster-than testing preorder based on continuous-time semantics coincides with the analogous testing preorder based on discrete-time semantics [12]. This result depends very much on the testing setting and is different from the sort of discretization obtained for timed automata [3].

Independently, Corradini *et al.* [11] pursued a different idea for relating processes with respect to speed, which is known as the *ill-timed-but-well-caused* approach [2, 14]. This approach allows system components to attach local time stamps to actions. Since actions may occur as in an untimed process algebra, local time stamps may decrease within a sequence of actions that is exhibited by several processes running in parallel. The presence of these "ill-timed" runs makes it difficult to technically relate the faster-than preorder of Corradini *et al.* to the one of Moller and Tofts. Note that simply restricting the setting of [11] to "well-timed" behavior does not suffice since, *e.g.*, this setting neither permits communication between processes, nor restriction and relabeling operators. Restricting a comparison to communication-free processes would be unsatisfactory due to the importance of the interplay between timing and communication for any approach to timed systems.

However, we believe that the fundamental idea of faster-than is the same in both approaches, Corradini *et al.* and ours, which consider absolute time and relative time, respectively. The present article makes this technically precise via the full-abstraction result (Thm. 4.4) which relates the amortized preorder that explicitly measures absolute time, with the MT-preorder that implicitly measures relative time.

9. Conclusions and Future Work

In previous work [19], the authors investigated bisimulation-based preorders that relate the speeds of asynchronous processes relative to upper time bounds, specifying when actions have to be executed at the latest. The present article considered the case of lower time bounds, specifying when actions may be executed at the earliest, by revisiting the seminal approach of Moller and Tofts [23]. Although Moller and Tofts' work was published more than a decade ago and the first one to introduce a faster-than relation in timed process algebra, it was never followed up in the literature — except for [1] where characteristic formulae for this preorder are provided. One reason for this might be the absence of strong theoretical results, including the absence of (i) a compositionality result for arbitrary processes, (ii) a full-abstraction result, and (iii) a complete axiomatization for finite processes, as well as the bleak picture drawn in [23] for achieving such results elegantly.

This article established these missing results by introducing a novel process– algebraic commutation lemma between action and clock transitions, as well as the idea of amortized faster–than relations. In particular, a full–abstraction theorem with respect to an intuitive amortized preorder that uses bookkeeping for deciding whether one process is faster than another was proved. In addition, an expansion law was established for finite processes, which paved the way for a sound and complete axiomatization of the Moller–Tofts preorder. This not only testifies to the nature of the MT–preorder but also highlights its importance among the sparse related work in the field. Last, but not least, a variant of the MT–preorder that abstracts from internal, unobservable computations was studied.

Future work should proceed along three directions. Firstly, we wish to complete the theory for our weak MT-precongruence by establishing the conjectured fullabstraction result. Secondly, the developed preorders should be implemented in a formal verification tool, such as the *Concurrency Workbench NC* [10]. Thirdly, we intend to integrate our theory for lower time bounds with our earlier work on upper time bounds [19], thereby exploring the appropriateness of our faster-than approaches for settings exhibiting restricted asynchrony.

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References

- L. Aceto, A. Ingólfsdóttir, M.L. Pedersen, and J. Poulsen. Characteristic formulae for timed automata. RAIRO, Theoretical Informatics and Applications, 34:565–584, 2000.
- [2] L. Aceto and D. Murphy. Timing and causality in process algebra. Acta Inform., 33(4):317– 350, 1996.
- [3] R. Alur and D. Dill. A theory of timed automata. TCS, 126(2):183-235, 1994.
- S. Arun-Kumar and M.C.B. Hennessy. An efficiency preorder for processes. Acta Inform., 29(8):737–760, 1992.
- [5] S. Arun-Kumar and V. Natarajan. Conformance: A precongruence close to bisimilarity. In STRICT '95, Workshops in Comp., pages 55–68. Springer-Verlag, 1995.

- [6] E. Badouel and P. Darondeau. On guarded recursion. TCS, 82(2):403–408, 1991.
- [7] J.C.M. Baeten and C.A. Middelburg. Process algebra with timing: Real time and discrete time. In Bergstra et al. [8], chapter 10, pages 627–684.
- [8] J.A. Bergstra, A. Ponse, and S.A. Smolka, editors. *Handbook of Process Algebra*. Elsevier Science, 2001.
- [9] R. Cleaveland, G. Lüttgen, and M. Mendler. An algebraic theory of multiple clocks. In CONCUR '97, volume 1243 of LNCS, pages 166–180. Springer-Verlag, 1997.
- [10] R. Cleaveland and S. Sims. The NCSU Concurrency Workbench. In CAV '96, volume 1102 of LNCS, pages 394–397. Springer-Verlag, 1996.
- [11] F. Corradini, R. Gorrieri, and M. Roccetti. Performance preorder and competitive equivalence. Acta Inform., 34(11):805–835, 1997.
- [12] F. Corradini, W. Vogler, and L. Jenner. Comparing the worst-case efficiency of asynchronous systems with PAFAS. Acta Informatica, 38:735–792, 2002.
- [13] R. De Nicola and M.C.B. Hennessy. Testing equivalences for processes. TCS, 34(1-2):83–133, 1984.
- [14] R. Gorrieri, M. Roccetti, and E. Stancampiano. A theory of processes with durational actions. TCS, 140(1):73–94, 1995.
- [15] M.C.B. Hennessy and T. Regan. A process algebra for timed systems. Inform. and Comp., 117(2):221–239, 1995.
- [16] C.A.R. Hoare. Communicating Sequential Processes. Prentice Hall, 1985.
- [17] L. Jenner and W. Vogler. Comparing the efficiency of asynchronous systems. In ARTS '99, volume 1601 of LNCS, pages 172–191. Springer-Verlag, 1999.
- [18] L. Jenner and W. Vogler. Fast asynchronous systems in dense time. TCS, 254(1-2):379–422, 2001.
- [19] G. Lüttgen and W. Vogler. Bisimulation on speed: Lower time bounds. In FOSSACS 2004, LNCS. Springer-Verlag, 2004.
- [20] R. Milner. A complete inference system for a class of regular behaviours. J. of Comp. and Sys. Sc., 28:439–466, 1984.
- [21] R. Milner. Communication and Concurrency. Prentice Hall, 1989.
- [22] F. Moller and C. Tofts. A temporal calculus of communicating systems. In CONCUR '90, volume 458 of LNCS, pages 401–415. Springer-Verlag, 1990.
- [23] F. Moller and C. Tofts. Relating processes with respect to speed. In CONCUR '91, volume 527 of LNCS, pages 424–438. Springer-Verlag, 1991.
- [24] V. Natarajan and R. Cleaveland. An algebraic theory of process efficiency. In LICS '96, pages 63–72. IEEE Computer Society Press, 1996.
- [25] S. Schneider. An operational semantics for timed CSP. Inform. and Comp., 116(2):193–213, 1995.
- [26] W. Vogler. Faster asynchronous systems. Information and Computation, 184:311–342, 2003.

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